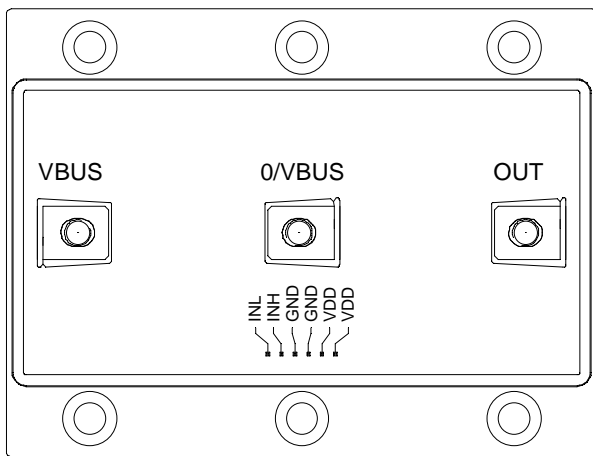
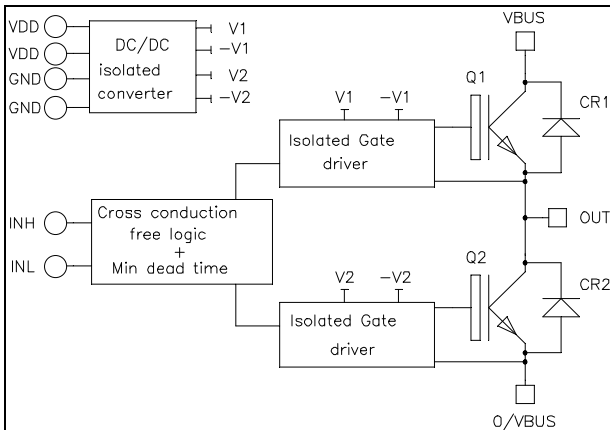


*Phase leg
Intelligent Power Module*

**$V_{CES} = 600V$
 $I_C = 400A @ T_c = 80^\circ C$**



Application

- Motor control
- Uninterruptible Power Supplies
- Switched Mode Power Supplies
- Amplifier

Features

- **Non Punch Through (NPT) FAST IGBT**
 - Low voltage drop
 - Low tail current
 - Soft recovery parallel diodes
 - Low diode VF
 - Low leakage current
 - RBSOA & SCSOA rated
- **Integrated Fail Safe IGBT Protection (Driver)**
 - Top Bottom input signals Interlock
 - Isolated DC/DC Converter

- Low stray inductance
- M5 power connectors
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Stable temperature behavior
- Very rugged
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Very high noise immunity (common mode rejection > 25kV/μs)
- Galvanic Isolation: 3750V for the optocoupler
2500V for the transformer
- 5V logic level with Schmitt-trigger Input
- Single $V_{DD}=5V$ supply required
- Secondary auxiliary power supplies internally generated (15V, -6V)
- Optocoupler qualified to AEC-Q100 test guidelines
- RoHS compliant

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

1. Inverter Power Module

Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V_{CES}	Collector - Emitter Breakdown Voltage	600	V
I_C	Continuous Collector Current	$T_C = 25^\circ\text{C}$	600
		$T_C = 80^\circ\text{C}$	400
I_{CM}	Pulsed Collector Current	$T_C = 25^\circ\text{C}$	800
P_D	Maximum Power Dissipation	$T_C = 25^\circ\text{C}$	1250
RBSOA	Reverse Bias Safe Operating Area	$T_j = 150^\circ\text{C}$	800A@550V

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I_{CES}	Zero Gate Voltage Collector Current	$V_{GE} = 0\text{V}$ $V_{CE} = 600\text{V}$	$T_j = 25^\circ\text{C}$		0.3	mA
			$T_j = 150^\circ\text{C}$		1	
$V_{CE(sat)}$	Collector Emitter Saturation Voltage	$V_{DD} = V_{IN} = 5\text{V}$ $I_C = 400\text{A}$	$T_j = 25^\circ\text{C}$	1.5	1.9	V
			$T_j = 150^\circ\text{C}$	1.7		

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{ies}	Input Capacitance	$V_{GE} = 0\text{V}$ $V_{CE} = 25\text{V}$ $f = 1\text{MHz}$		24		nF
C_{oes}	Output Capacitance			1.6		
C_{res}	Reverse Transfer Capacitance			0.8		
T_r	Rise Time	Inductive Switching (25°C) $V_{DD} = V_{IN} = 5\text{V}$ $V_{Bus} = 300\text{V}; I_C = 400\text{A}$		45		ns
T_f	Fall Time			55		
T_r	Rise Time	Inductive Switching (125°C) $V_{DD} = V_{IN} = 5\text{V}$ $V_{Bus} = 300\text{V}$ $I_C = 400\text{A}$		25		ns
T_f	Fall Time			70		
E_{on}	Turn-on Switching Energy	$V_{DD} = V_{IN} = 5\text{V}; V_{Bus} = 360\text{V}$ $t_p \leq 6\mu\text{s}; T_j = 150^\circ\text{C}$		3.5		mJ
E_{off}	Turn-off Switching Energy			14		
I_{sc}	Short Circuit data			2000		A
R_{thJC}	Junction to Case thermal resistance				0.12	$^\circ\text{C}/\text{W}$

Reverse diode ratings and characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V _{RRM}	Maximum Peak Repetitive Reverse Voltage		600			V
I _{RM}	Maximum Reverse Leakage Current	V _R =600V			350	μA
					500	
I _F	DC Forward Current			400		A
V _F	Diode Forward Voltage	I _F = 400A		1.6	2	V
				1.5		
t _{rr}	Reverse Recovery Time			125		ns
				220		
Q _{rr}	Reverse Recovery Charge	I _F = 400A V _R = 300V di/dt = 4800A/μs		19		μC
				40		
E _{rr}	Reverse Recovery Energy			4.4		mJ
				9.6		
R _{thJC}	Junction to Case thermal resistance				0.20	°C/W

2. Driver
Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V _{DD}	Supply Voltage	5.5	V
V _{INi}	Input signal voltage i=L, H	5.5	V
I _{VDDmax}	Maximum Supply current	0.35	A
	V _{INi} = 0V, i = L & H		
	V _{DD} = 5V, V _{INH} = V _{INL} ; F _{out} = 45kHz	2	
f _{max}	Maximum Switching Frequency	45	kHz

Driver Electrical Characteristics

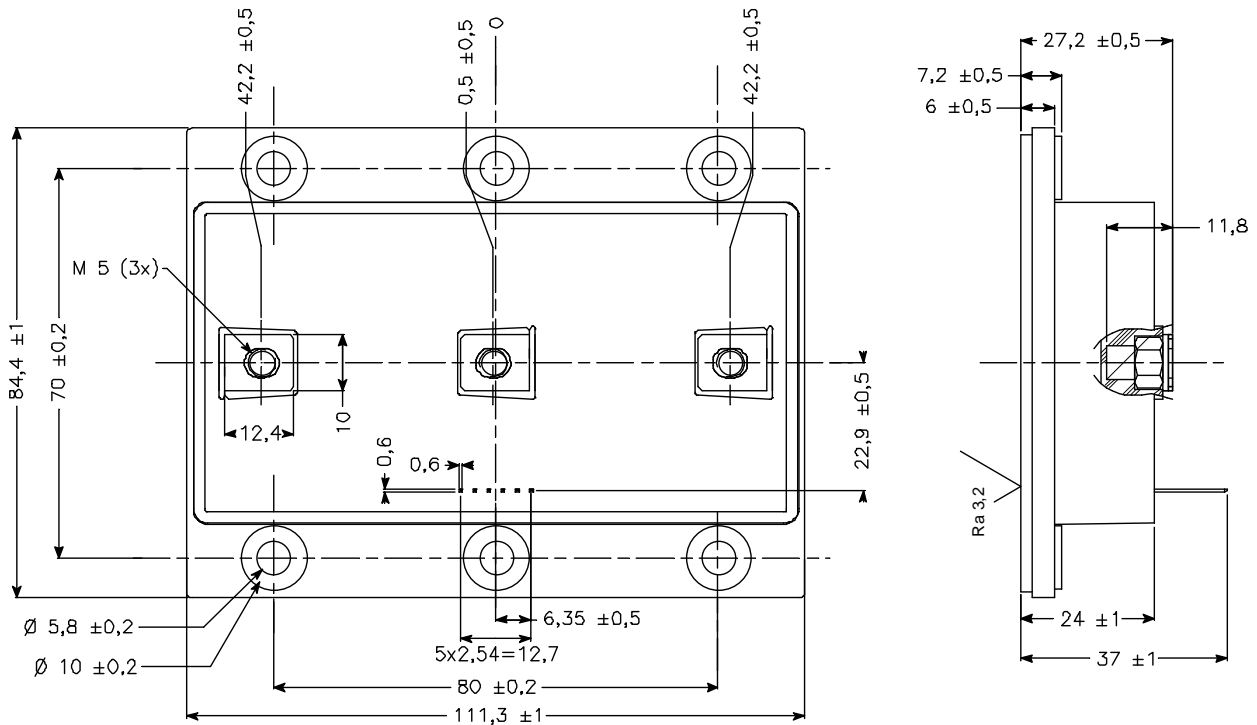
Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V _{DD}	Operating Supply Voltage		4.5	5	5.5	V
V _{INi(max)}	Maximum Input Voltage		-0.5	5	5.5	V
V _{INi(th+)}	Positive Going Threshold Voltage	i = L, H		3.2		V
V _{INi(th-)}	Negative Going Threshold Voltage			1		V
R _{INi}	Input Resistance *			1		kΩ
T _{d(on)}	Turn On delay time	Driver + IGBT		1100 [Ⓞ]		ns
D _T	Built in dead time			600		ns
T _{d(off)}	Turn Off delay time	Driver + IGBT		750		ns
PWD	Pulse Width Distortion				300	ns
PDD	Propagation Delay Difference between any two driver	T _{d(on)} - T _{d(off)}	-350		350	ns
V _{ISOL}	Primary to Secondary Isolation		2500			V _{RMS}

* Low impedance guarantees good noise immunity.

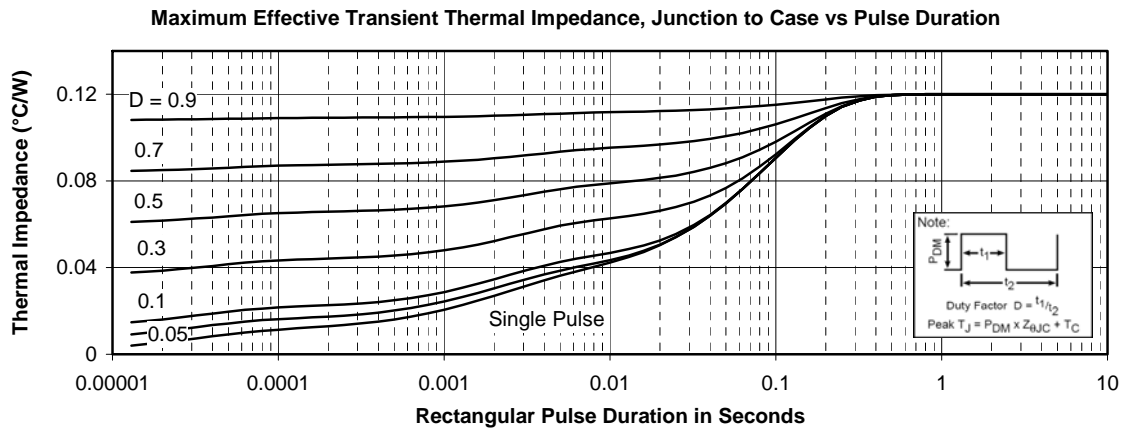
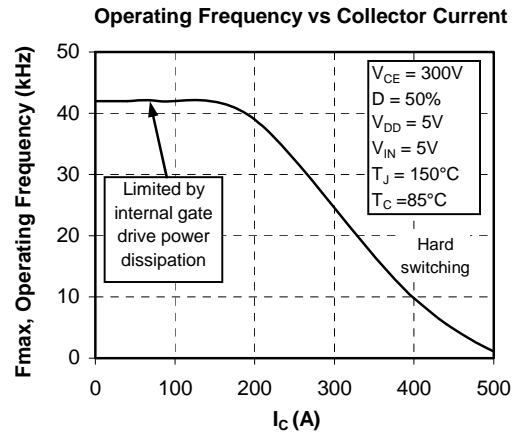
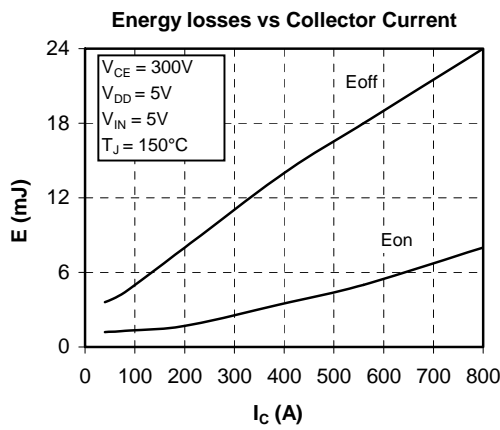
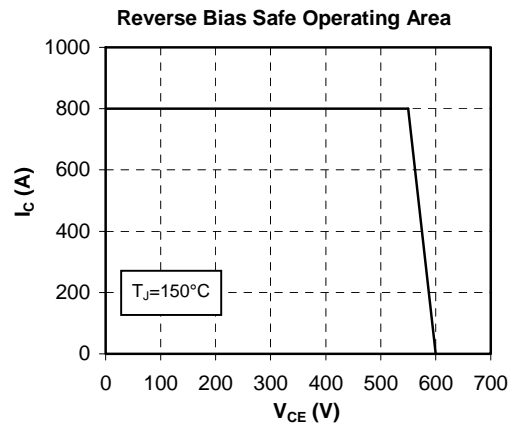
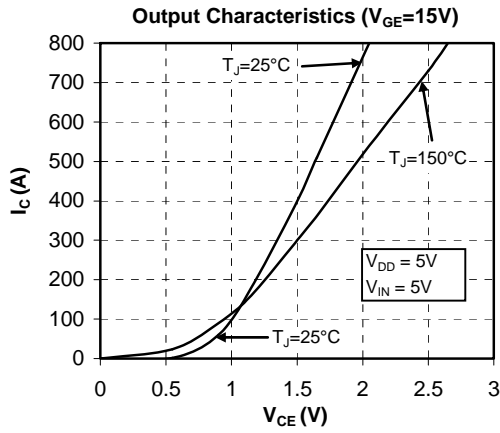
3. Package characteristics

Symbol	Characteristic	Min	Typ	Max	Unit	
V _{ISOL}	RMS Isolation Voltage, any terminal to case t=1 min, I isol<1mA, 50/60Hz	2500			V	
T _J	Operating junction temperature range	-40		150	°C	
T _{OP}	Operating Ambient Temperature	-40		85		
T _{STG}	Storage Temperature Range	-40		100		
T _C	Operating Case Temperature	-40		100		
Torque	Mounting torque	To heatsink	M5	2	4.7	N.m
		For terminals	M5	2	4	
Wt	Package Weight		550		g	

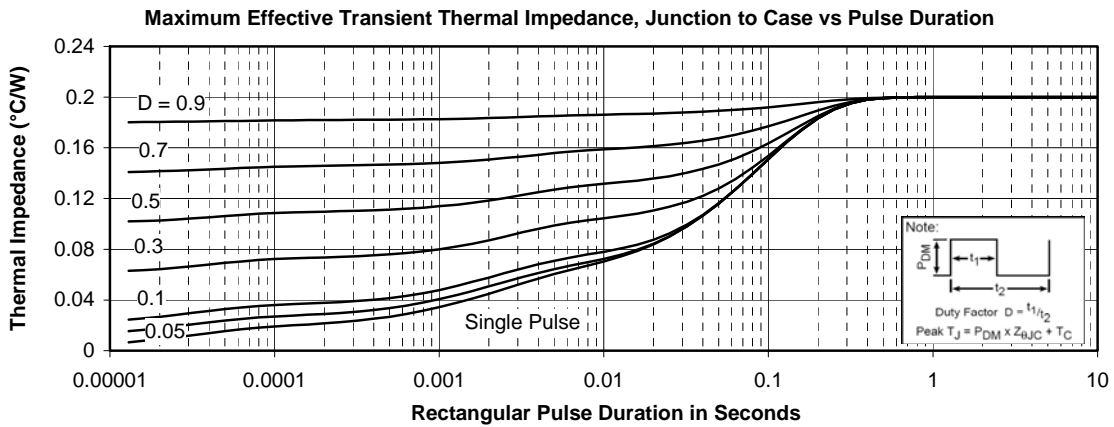
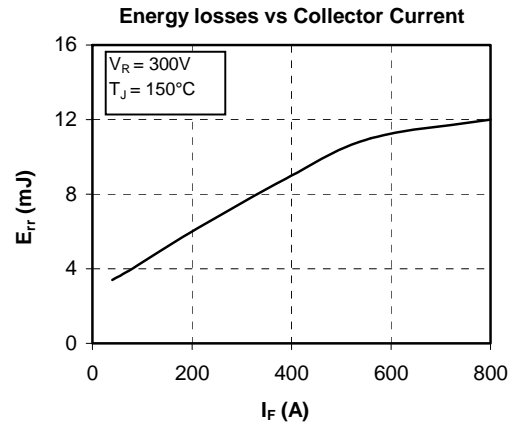
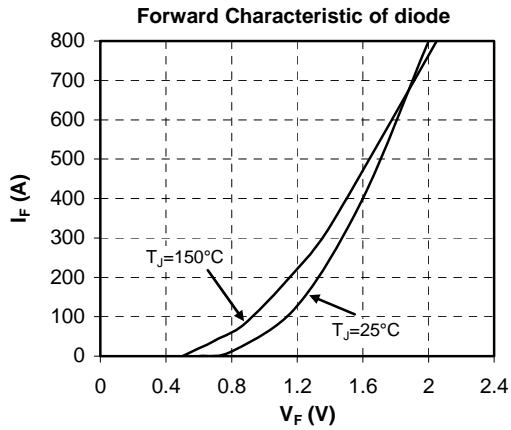
4. LP8 Package outline (dimensions in mm)



Typical IGBT Performance Curve



Typical diode Performance Curve



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