Miniature, 1W Isolated REGULATED DC/DC CONVERTERS

FEATURES

- UL1950 RECOGNIZED
- DIP-18, SO-28
- 53W/in³ (3.3W/cm³) POWER DENSITY
- DEVICE-TO-DEVICE SYNCHRONIZATION
- THERMAL PROTECTION
- 1000Vrms ISOLATION
- 400kHz SWITCHING
- 125 FITS AT 55°C
- ±10% INPUT RANGE
- SHORT-CIRCUIT PROTECTED
- 5V, 12V, 24V INPUTS
- 3.3V, 5V OUTPUTS
- HIGH EFFICIENCY

APPLICATIONS

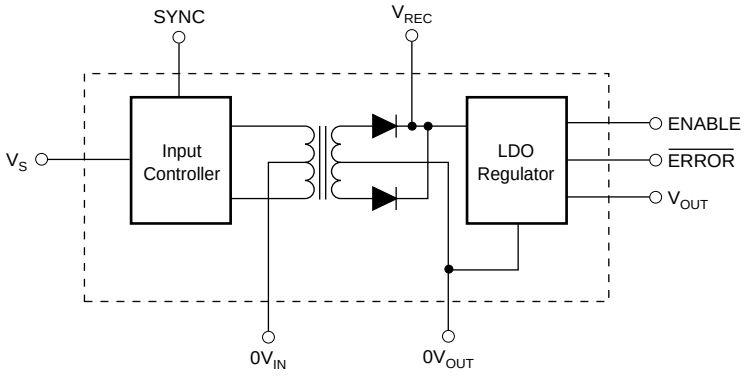
- POINT-OF-USE POWER CONVERSION
- DIGITAL INTERFACE POWER
- GROUND LOOP ELIMINATION
- POWER-SUPPLY NOISE REDUCTION

DESCRIPTION

The DCR01 family is a series of high-efficiency, input-isolated, output-regulated DC/DC converters. In addition to 1W nominal, galvanically-isolated output power capability, this range of DC/DCs offer very low output noise, thermal protection, and high accuracy.

The DCR01 family is implemented in standard molded IC packaging, giving standard JEDEC outlines suitable for high-volume assembly.

The DCR01 is manufactured using the same technology as standard IC packages, thereby achieving very high reliability.



°CLead Temperature (soldering, 1ls).....

ELECTRICAL CHARACTERISTICS (Cont.)

At T_A = +25°C, V_S = nominal, I_O = 10mA, C_O = 0.1µF, unless otherwise specified.

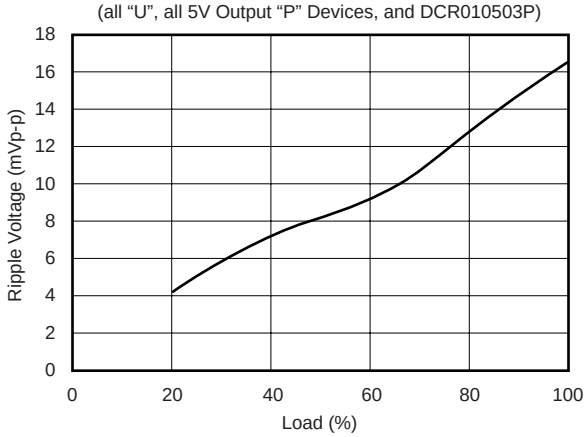
PARAMETER	CONDITIONS	DCR01 SERIES MIN	DCR01 SERIES TYP	DCR01 SERIES MAX	UNITS
Supply Current					
DCR010503P	I_O = 0mA		18		mA
	I_O = 10mA		28		mA
	I_O = 300mA		335		mA
DCR010503U	I_O = 0mA		24		mA
	I_O = 10mA		33		mA
	I_O = 300mA		339		mA
DCR010505P	I_O = 0mA		25		mA
	I_O = 10mA		40		mA
	I_O = 200mA		306		mA
DCR010505U	I_O = 0mA		25		mA
	I_O = 10mA		40		mA
	I_O = 200mA		306		mA
DCR011203P	I_O = 0mA		13		mA
	I_O = 10mA		17		mA
	I_O = 390mA		173		mA
DCR011203U	I_O = 0mA		13		mA
	I_O = 10mA		17		mA
	I_O = 390mA		136		mA
DCR011205P	I_O = 0mA		13		mA
	I_O = 10mA		18		mA
	I_O = 200mA		125		mA
DCR011205U	I_O = 0mA		14		mA
	I_O = 10mA		19		mA
	I_O = 200mA		123		mA
DCR012403P	I_O = 0mA		17		mA
	I_O = 10mA		18		mA
	I_O = 390mA		97		mA
DCR012403U	I_O = 0mA		15		mA
	I_O = 10mA		17		mA
	I_O = 390mA		75		mA
DCR012405P	I_O = 0mA		15		mA
	I_O = 10mA		18		mA
	I_O = 200mA		69		mA
DCR012405U	I_O = 0mA		15		mA
	I_O = 10mA		18		mA
	I_O = 200mA		67		mA
Reflected Ripple Current	20MHz Bandwidth, 100% Load C_{IN} = 2.2µF, C_{FILTER} = 1µF		8		mAp-p
ISOLATION					
Voltage	1s Flash Test	1			kVrms
	60s Test, UL1950[2]	1			kVrms
Input/Output Capacitance			25		pF
OUTPUT ENABLE CONTROL					
Logic High Input Voltage		2.0		V_{REC}	V
Logic High Input Current	2.0 < V_{ENABLE} < V_{REC}		100		nA
Logic Low Input Voltage		–0.2		0.5	V
Logic Low Input Current	0 < V_{ENABLE} < 0.5		100		nA
V_{REC}	All 3.3V Outputs		3.3		V
V_{REC}	All 5V Outputs		5		V
ERROR FLAG					
Logic High Open Collector Leakage	V_{ERROR} = 5V			10	µA
Logic Low Output Voltage	Sinking 2mA			0.4	V
THERMAL SHUTDOWN					
Junction Temperature					
Temperature Activated			150		°C
Temperature Deactivated			130		°C
SYNCHRONIZATION PIN					
Max External Capacitance on SYNC Pin				3	pF
Internal Oscillator Frequency		720	800	880	kHz
External Synchronization Frequency		720		880	kHz
External Synchronization Signal High		2.5	2.5	5.0	V
External Synchronization Signal Low		0		0.4	V
TEMPERATURE RANGE					
Operating		–40		+85	°C

NOTES: (1) C_{IN} = 2.2µF, C_{FILTER} = 1µF, C_{OUT} = 0.1µF. (2) During UL approval only.

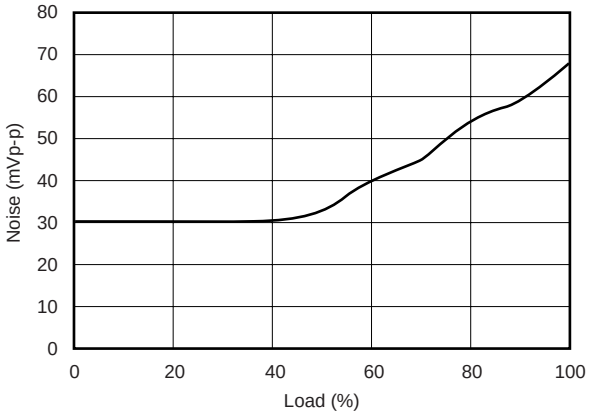
TYPICAL CHARACTERISTICS

At T_A = +25°C, V_S = 5V, I_O = 10mA, C_{FILTER} = 1µF, C_O = 0.1µF, unless otherwise specified.

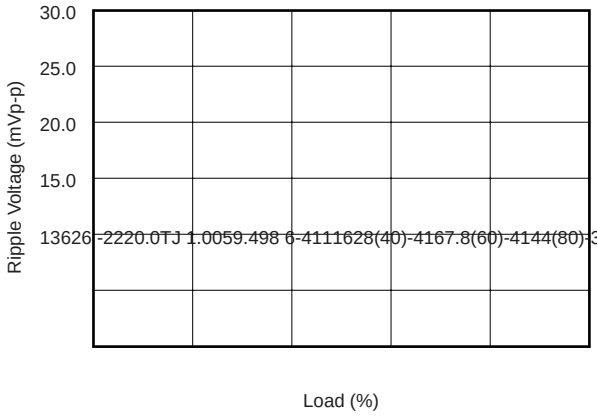
OUTPUT VOLTAGE RIPPLE
(all “U”, all 5V Output “P” Devices, and DCR010503P)



DCR0105 OUTPUT NOISE
(100MHz Bandwidth)



DCR011203P AND DCR012403P
OUTPUT VOLTAGE RIPPLE

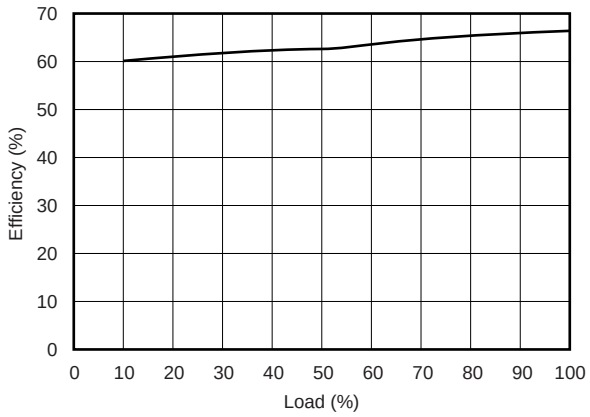


13626 -2220.0TJ 1.0059.498 6-4111628(40)-4167.8(60)-4144(80)-3871.9(100)]TJ -5.21 0 TD (0)Tj ET 1 w 335.995 564.16137.22.318 41.4421960 Tw3c 4559 390.0(Rip Tc 0v 94 w

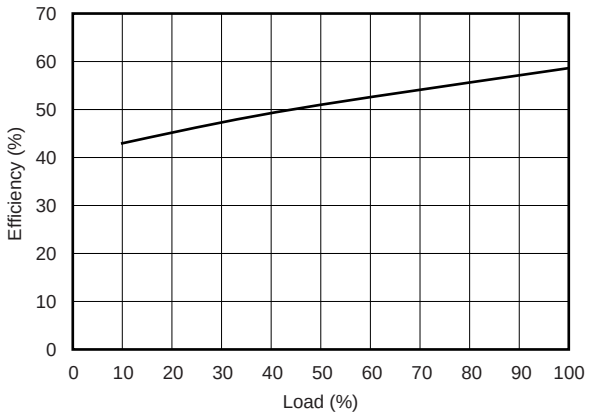
TYPICAL CHARACTERISTICS (Cont.)

At T_A = +25°C, V_S = 5V, I_O = 10mA, C_{FILTER} = 1µF, C_O = 0.1µF, unless otherwise specified.

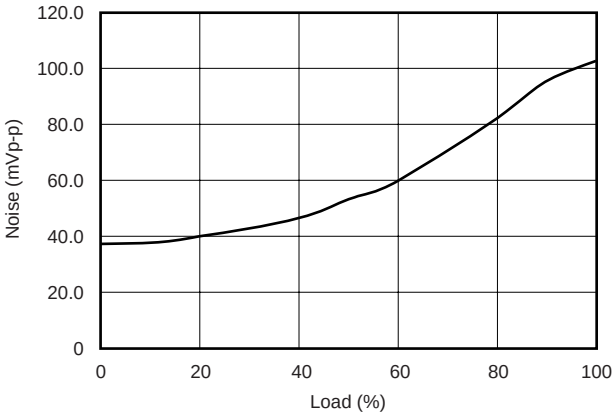
EFFICIENCY FOR 5V AND 12V INPUT
"U" DEVICES, 5V OUTPUT



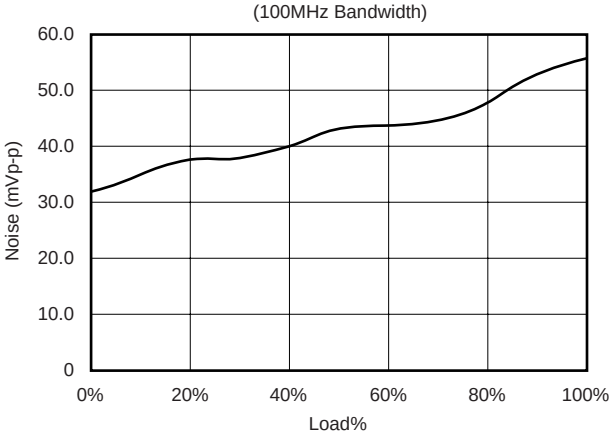
EFFICIENCY vs LOAD
DCR0112405 "P" and "U" DEVICES



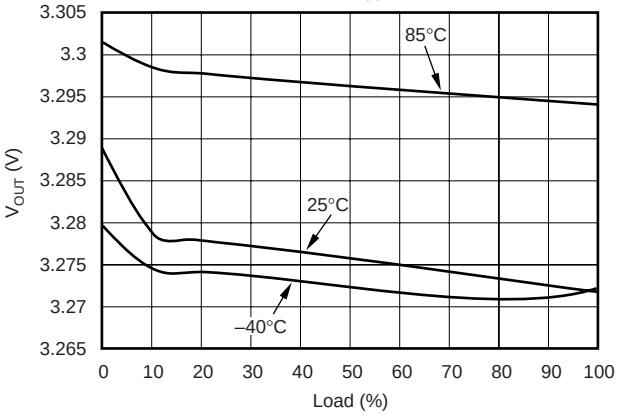
DCR011203P OUTPUT NOISE
(100MHz Bandwidth)



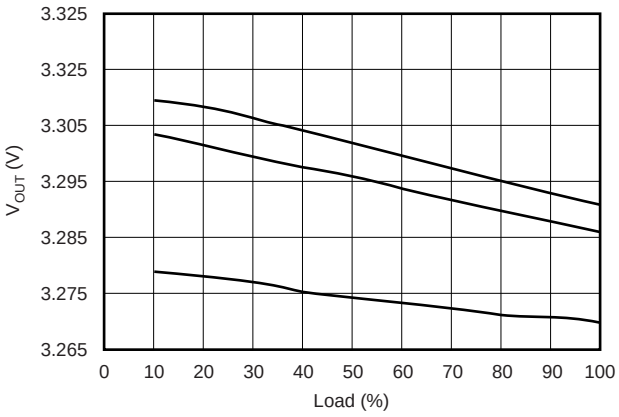
DCR011205P OUTPUT NOISE
(100MHz Bandwidth)

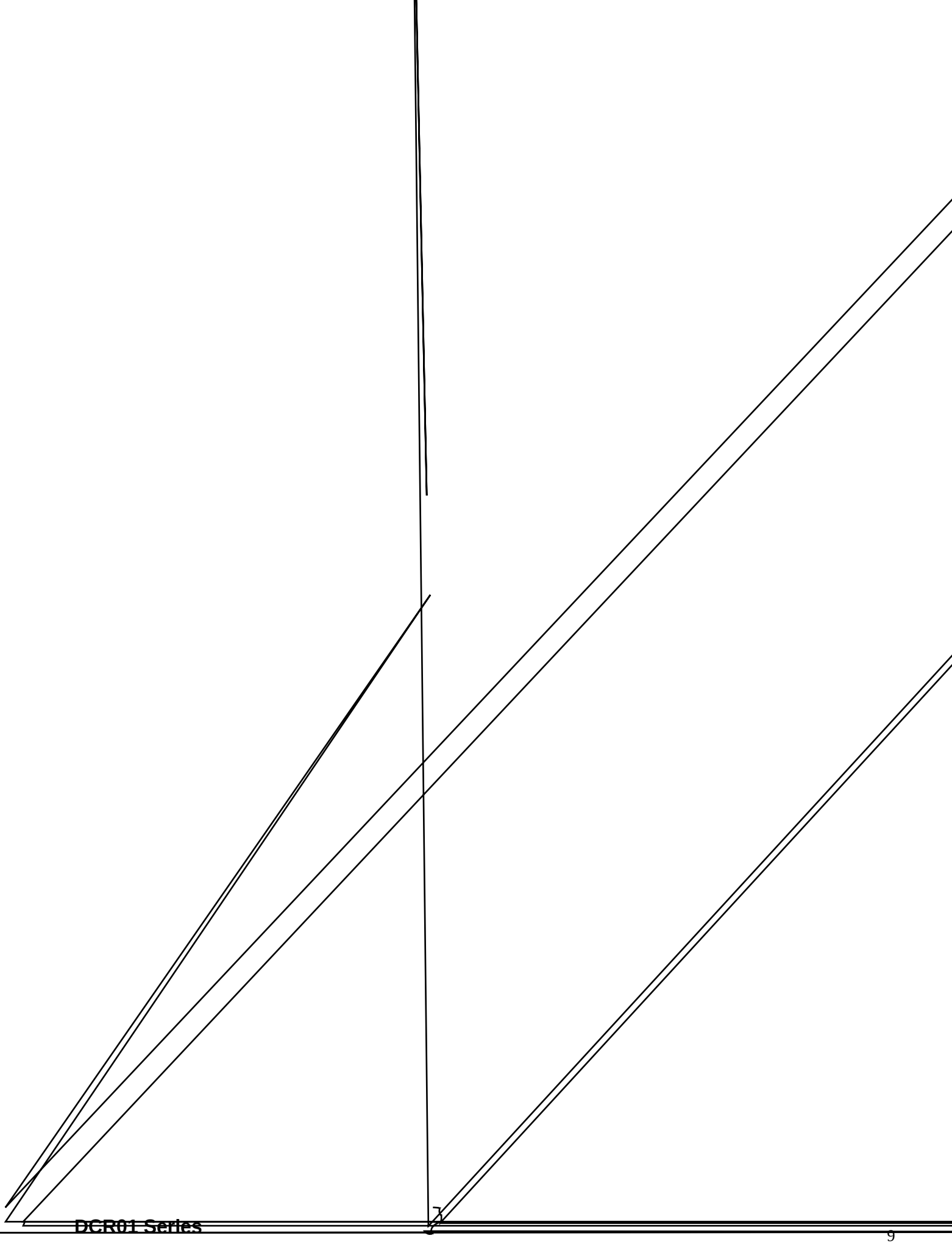


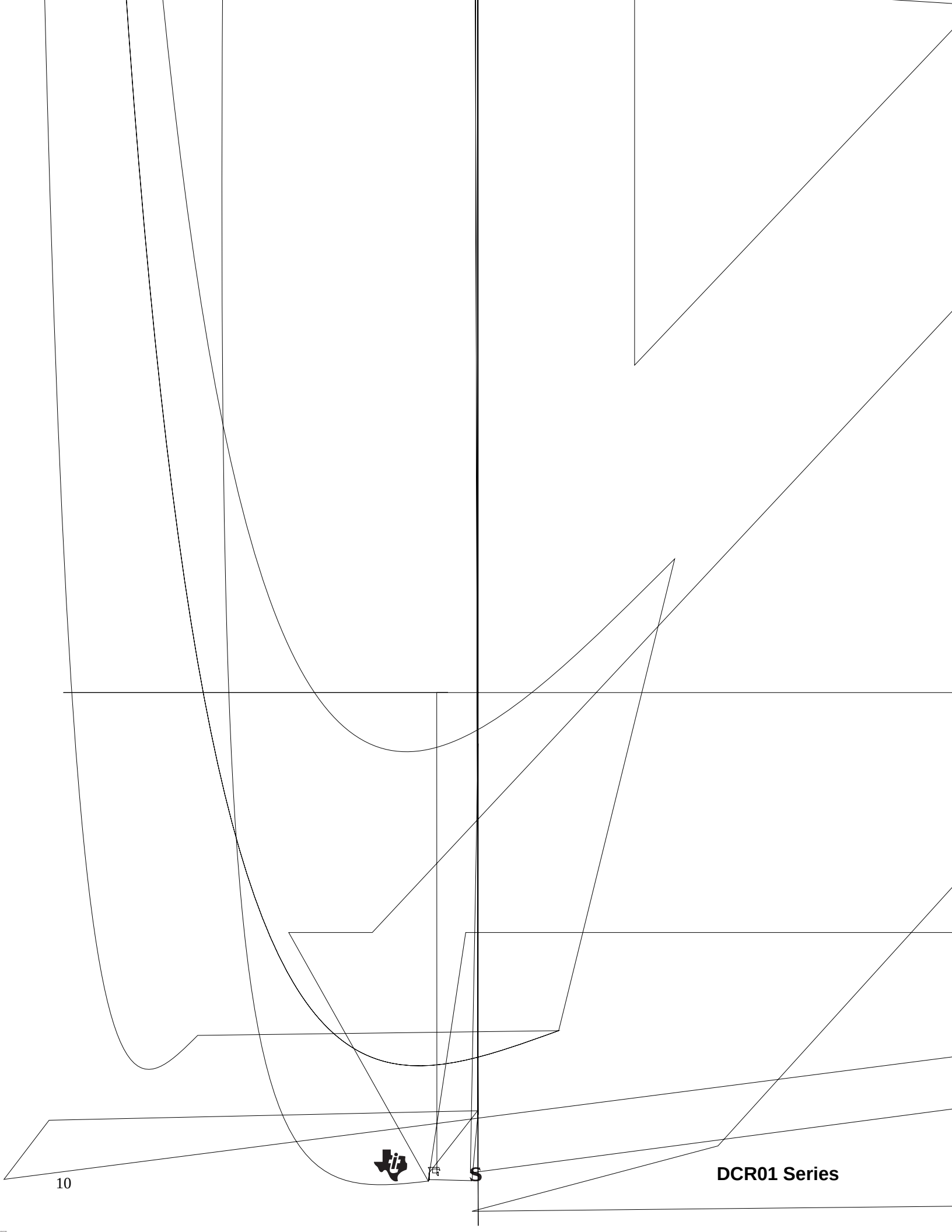
DCR010503P V_{OUT} vs LOAD



3V OUTPUT vs LOAD
(all devices except DCR010503P)

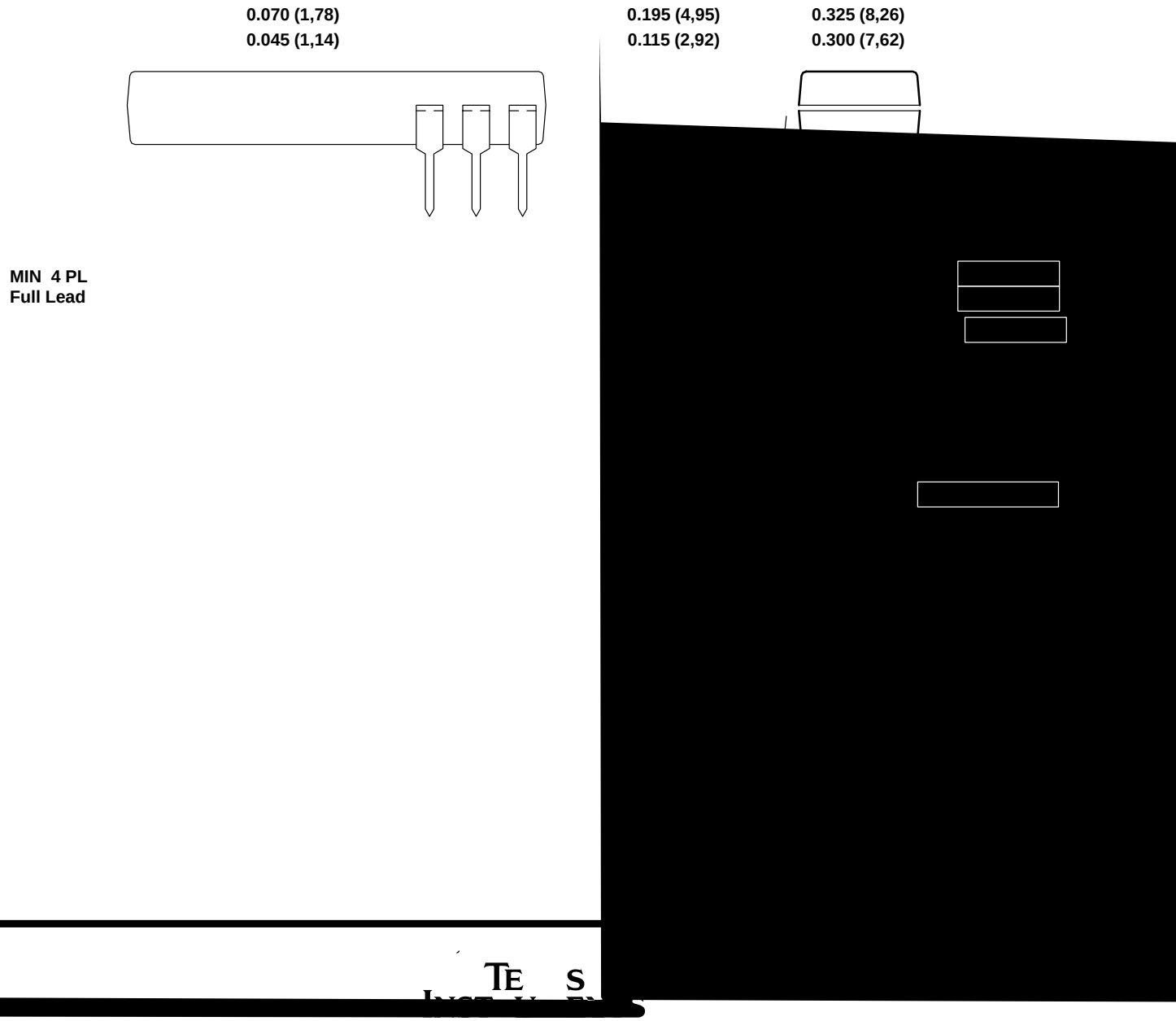






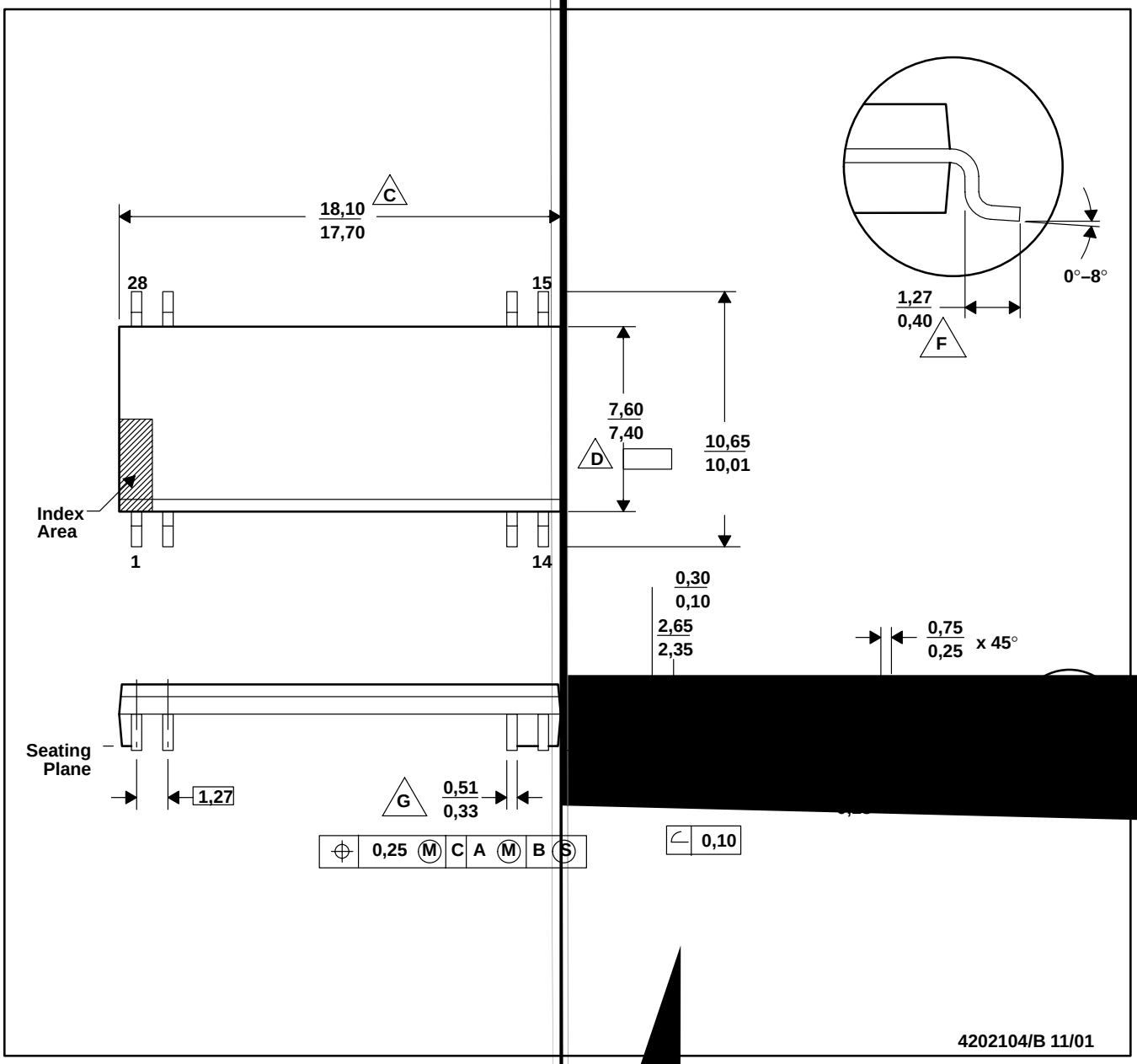
MPDI055 – APRIL 2001

NVE (R-PDIP-T10/18) **PLASTIC DUAL-IN-LINE**



MPDS106A – AUGUST 2001 – REVISED NOVEMBER 2001

DVB(R-PDSO-G12/28) **PLASTIC SMALL-OUTLINE**



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