



### **Photocouplers**

LTV-0601(Preliminary Version)
Data Sheet

Created Date: 01/02/2012 Revision: 1.0, 01/02/2012



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http://www.liteon.com/opto



#### . DESCRIPTION

The LTV-0601 consists of a high efficient AlGaAs Light Emitting Diode and a high speed optical detector. This design provides excellent AC and DC isolation between the input and output sides of the Optocoupler. The output of the optical detector features an open collector Schottky clamped transistor. The enable function allows the optical detector to be strobed. A guaranteed common mode transient immunity is up to 15,000V/µs.

The Optocoupler operational parameters are guaranteed over the temperature range from -40°C ~ +85°C.

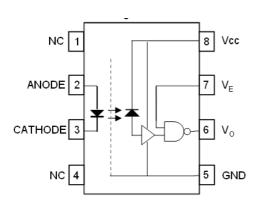
#### 1.1 Features

- SO8 package
- High speed 10MBd typical
- Guaranteed AC and DC performance over temperature -40°C ~ +85°C.
- LSTTL/TTL Compatible.
- Strobable output.

#### 1.2 Applications

- Isolation in line receivers
- Ground loop elimination
- Feedback Element in Switching Mode Power Supplier
- High Speed Logic Ground Isolation TTL/TTL, TTL/CMOS, TTL/LSTTL
- Pulse transformer replacement
- Power transistor isolation in motor drives
- Interface between Microprocessor system, computer and their peripheral

#### 1.3 Functional Diagram



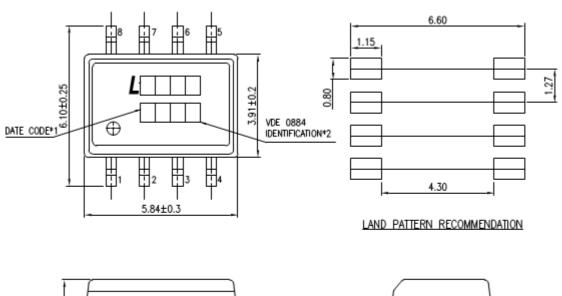
Truth Table (Positive Logic)

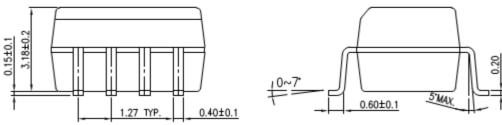
LED	ENABLE	OUT
ON	Н	L
OFF	Н	Н
ON	L	Н
OFF	L	Н
ON	NC	L
OFF	NC	Н

A 0.1µF bypass Capacitor must be connected between Pin8 and Pin5



#### 2. PACKAGE DIMENSIONS





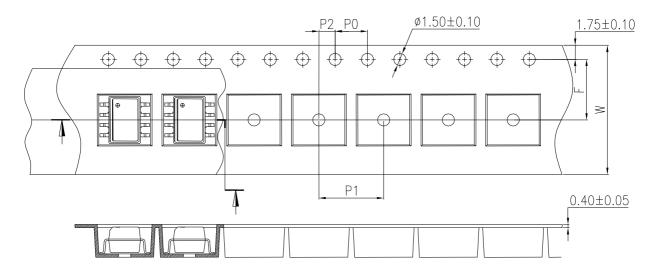
Part No: LTV-0601

#### Notes:

- 1. Date code
- 2. "V" to represent VDE0884
- 3. Date code
- 4. Dimensions are all in Millimeters.



### 3. TAPING DIMENSIONS



Description	Symbol	Dimension in mm (inch)
Tape wide	W	16±0.3 (0.63)
Pitch of sprocket holes	P <sub>0</sub>	4±0.1 (0.15)
Distance of compartment	F	7.5±0.1 (0.295)
Distance of compartment	P <sub>2</sub>	2±0.1 (0.079)
Distance of compartment to compartment	P <sub>1</sub>	8±0.1 (0.47)

#### **Quantities Per Reel**

Package Type	LTV-0601
Quantities (pcs)	2000



#### 4. RATING AND CHARACTERISTICS

#### 4.1 Absolute Maximum Ratings at Ta=25℃ \*1

	Parameter	Symbol	Rating	Unit
	Average Forward Input Current	I <sub>F</sub>	20	mA
	Reverse Input Voltage	$V_R$	5	V
Input	Power Dissipation	Pı	35	mW
	Enable Input Voltage	$V_{E}$	Vcc+5	V
	Enable Input current	Ι <sub>Ε</sub>	5	mA
	Output Collector Current	lo	50	mA
Output	Output Collector Voltage	Vo	7	V
	Output Collector Power Dissipation	P <sub>o</sub>	85	mW
	Isolation Voltage	V <sub>iso</sub>	3750	$V_{rms}$
	Supply Voltage	$V_{CC}$	7	V
	Operating Temperature	$T_{opr}$	-55 ~ +85	$^{\circ}\!\mathbb{C}$
	Storage Temperature	$T_{stg}$	-55 ~ +125	$^{\circ}\!\mathbb{C}$
	Lead Solder Temperature *2	$T_{sol}$	260	$^{\circ}$

- Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can
  cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely
  affect reliability.
- 2. 260°C for 10 seconds. Refer to Lead Free Reflow Profile.



### **4.2 Recommended Operating Conditions**

Parameter	Symbol	Min	Max	Unit
Operating Temperature	T <sub>A</sub>	-40	85	°C
Supply Voltage	V <sub>cc</sub>	4.5	5.5	V
Low Level Input Current	I <sub>FL</sub>	0	250	μΑ
High Level Input Current	I <sub>FH</sub>	5	15	mA
Low Level Enable Voltage	$V_{EL}$	0	0.8	V
High Level Enable Voltage	$V_{EH}$	2	V <sub>CC</sub>	V
Output Pull-up Resistor	$R_L$	330	4k	Ω
Fan Out (at $R_L$ =1k $\Omega$ per channel)	N	_	5	TTL Loads



#### 4.3 ELECTRICAL OPTICAL CHARACTERISTICS at Ta=25℃

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition	
Input							
Input Forward Voltage	$V_{F}$	_	1.38	1.8	V	I <sub>F</sub> = 10mA	
Input Forward Voltage	A)/ / A T		4.0		mV/ <sup>O</sup>	1 40 1	
Temperature Coefficient	$\Delta V_F / \Delta T$	_	-1.6	_	С	I <sub>F</sub> = 10mA	
Input Reverse Voltage	BV <sub>R</sub>	5.0	_	_	V	I <sub>R</sub> = 10μΑ	
Innut Threehold Current		_	1.35	5	mA	$V_E = 2V, V_{CC} = 5.5V,$	
Input Threshold Current	I <sub>TH</sub>	_	2 <sup>(1)</sup>	3	mA	I <sub>OL</sub> (sinking) = 13mA	
Input Capacitance	C <sub>IN</sub>	_	34	_	pF	$f = 1MHz, V_F = 0V$	
Detector							
						V <sub>E</sub> = 0.5V, V <sub>CC</sub> = 5.5V,	
High Level Supply Current	Іссн	_	7.4	10	uA	I <sub>F</sub> = 0mA	
Low Level Supply Current	Iccl	_	10	13	mA	$V_{E}$ = 0.5V, $V_{CC}$ = 5.5V,	
Low Level oupply ourient	ICCL		10	10	1117 (	I <sub>F</sub> =10mA	
High Level Enable Current	I <sub>EH</sub>	_	-0.6	-1.6	mA	$V_E = 2V$	
Low Level Enable Current	I <sub>EL</sub>	_	-0.9	-1.6	mA	V <sub>E</sub> = 0.5V	
High Level Enable Voltage	$V_{EH}$	2	_	_	V		
Low Level Enable Voltage	$V_{EL}$		_	0.8	V		
High Level Output Current	I <sub>OH</sub>			100	μA	$V_E = 2V, V_{CC} = 5.5V,$	
riigii Level Output Gurrent	IОН	_	_	100	μΛ	$V_O = 5.5V$ , $I_F = 250\mu A$	
		_				$V_E = 2V, V_{CC} = 5.5V,$	
Low Level Output Voltage	V <sub>OL</sub>		0.25	0.60	V	$I_F = 5mA$ ,	
						I <sub>OL</sub> (sinking) = 13mA	

Specified over recommended temperature ( $T_A = -40^{\circ}\text{C}$  to +85°C) unless otherwise specified. Typical values applies to  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}\text{C}$ . See note 1.

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### **5. SWITCHING SPECIFICATION**

T<sub>A</sub>=0~70°C, Vcc=5V, unless otherwise specified.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition	Note
Propagation Delay Time to Low Output Level	t <sub>PHL</sub>	25	40	100	ns	$R_L = 350\Omega$ , $C_L = 15pF$	3
Propagation Delay Time to High	t <sub>PLH</sub>	25	27	100	ns	$R_L = 350\Omega$ , $C_L = 15pF$	4
Pulse Width Distortion	t <sub>PLH</sub> -	_	12	_	ns	$R_L = 350\Omega, C_L = 15pF$	_
Propagation Delay Skew	t <sub>PSK</sub>	_				$R_L = 350\Omega, C_L = 15pF$	_
Output Rise Time (10 to 90%)	t <sub>r</sub>	_	20	_	ns	$R_L = 350\Omega$ , $C_L = 15pF$	_
Output Fall Time (90 to 10%)	t <sub>f</sub>	_	6.6	_	ns	$R_L = 350\Omega$ , $C_L = 15pF$	_
Propagation Delay Time of Enable from $V_{EH}$ to $V_{EL}$	t <sub>ELH</sub>	_	28	_	ns	$R_L = 350\Omega$ , $C_L = 15pF$ , $V_{EL} = 0V$ , $V_{EH} = 3V$	5
Propagation Delay Time of Enable from $V_{\text{EL}}$ to $V_{\text{EH}}$	t <sub>EHL</sub>	_	12	_	V/µs	$R_L = 350\Omega$ , $C_L = 15pF$ , $V_{EL} = 0V$ , $V_{EH} = 3V$	6
Logic High Common Mode  Transient Immunity	CM <sub>H</sub>	1,000	_	_	V/µs	$ V_{CM}  = 20V, V_{CC}=5V,$ $I_F = 0mA, V_{O(MIN)}=2V,$ $R_L = 350\Omega, T_A = 25^{\circ}C$	7,9
Logic Low Common Mode Transient Immunity	CM <sub>L</sub>	1,000	_	_	ns	$ V_{CM}  = 20V, V_{CC}=5V,$ $I_F=7.5mA, V_{O(MIN)}=0V,$ $R_L = 350\Omega, T_A = 25^{\circ}C$	8,9

<sup>\*</sup>All Typical at  $T_A$ =25°C



#### 6. ISOLATION CHARACTERISTIC

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition	Note
Input-Output Insulation Leakage	l. <sub>-</sub>			1.0		45% RH, t = 5s,	10,11
Current	I <sub>I-O</sub>	_		1.0	μA	$V_{I-O} = 3kV DC, T_A = 25^{\circ}C$	10,11
Withstand Insulation Test	$V_{ISO}$	3750			V	RH ≤ 50%, t = 1min,	10 11 12
Voltage	VISO	3750	_	_	V <sub>RMS</sub>	T <sub>A</sub> = 25°C	10,11,12
Input-Output Resistance	R <sub>I-O</sub>	_	10 <sup>12</sup>	_	Ω	V <sub>I-O</sub> = 500V DC	10
Input-Output Capacitance	C <sub>I-O</sub>	_	1.0	_	р	f = 1MHz, T <sub>A</sub> = 25°C	10

Specified over recommended temperature ( $T_A = -40^{\circ}C$  to  $+85^{\circ}C$ ) unless otherwise specified. Typical values applies to  $T_A = 25^{\circ}C$ 

#### Notes

- 1. A  $0.1\mu F$  or bigger bypass capacitor for  $V_{CC}$  is needed as shown in Fig.1
- 2. Peaking driving circuit may be used to speed up the LED. The peak drive current of LED may go up to 50mA and maximum pulse width 50ns, as long as average current doesn't exceed 20mA.
- 3.  $t_{PLH}$  (propagation delay) is measured from the 3.75 mA point on the falling edge of the input pulse to the 1.5 V point on the rising edge of the output pulse.
- 4. t<sub>PHL</sub> (propagation delay) is measured from the 3.75 mA point on the rising edge of the input pulse to the 1.5 V point on the falling edge of the output pulse.
- 5. The  $t_{\text{ELH}}$  enable propagation delay is measured from the 1.5 V point on the falling edge of the enable input pulse to the 1.5 V point on the rising edge of the output pulse.
- 6. The  $t_{EHL}$  enable propagation delay is measured from the 1.5 V point on the rising edge of the enable input pulse to the 1.5 V point on the falling edge of the output pulse.
- 7.  $CM_H$  is the maximum tolerable rate of rise of the common mode voltage to assure that the output will remain in a high logic state (i.e., VO > 2.0 V).
- 8.  $CM_L$  is the maximum tolerable rate of fall of the common mode voltage to assure that the output will remain in a low logic state (i.e., VO < 0.8 V).
- 9. No external pull up is required for a high logic state on the enable input. If the enable pin is not used, tying it to  $V_{\text{CC}}$ .
- 10. Device is considered a two-terminal device: pins 1, 2, 3, and 4 shorted together, and pins 5, 6, 7, and 8 shorted together.
- 11. In accordance with UL1577, each optocoupler is proof tested by applying an insulation test voltage 3000 V rms for one second (leakage current less than 5 uA). This test is performed before the 100% production test for partial discharge

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#### **SWITCHING TIME TEST CIRCUIT**

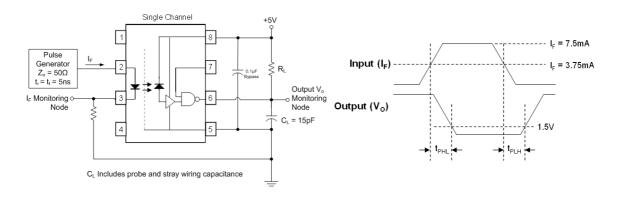


Figure 1: Single Channel Test Circuit for tPHL and tPLH

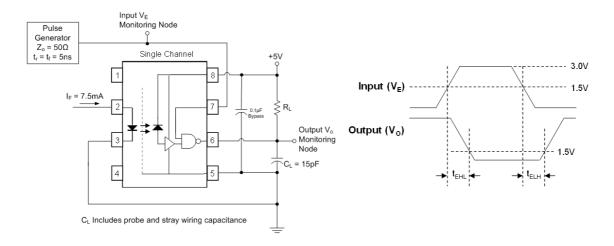


Figure 2: Single Channel Test Circuit for Common Mode Transient Immunity

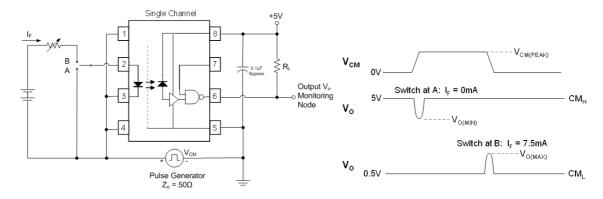


Figure 3: Single Channel Test Circuit for Common Mode Transient Immunity

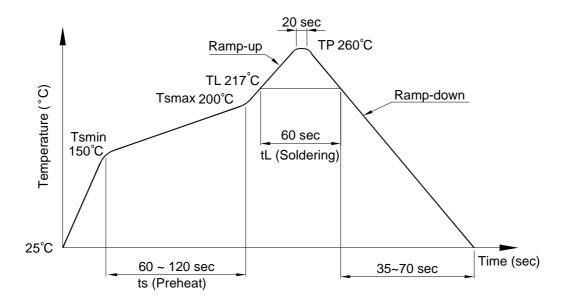


#### 8. TEMPERATURE PROFILE OF SOLDERING

#### 8.1 IR Reflow soldering (JEDEC-STD-020C compliant)

One time soldering reflow is recommended within the condition of temperature and time profile shown below. Do not solder more than three times.

Profile item	Conditions
Preheat	
- Temperature Min (T <sub>Smin</sub> )	150°C
- Temperature Max (T <sub>Smax</sub> )	200°C
- Time (min to max) (ts)	90±30 sec
Soldering zone	
- Temperature (T <sub>L</sub> )	217°C
- Time (t <sub>L</sub> )	60 sec
Peak Temperature (T <sub>P</sub> )	260°C
Ramp-up rate	3°C / sec max.
Ramp-down rate	3~6°C / sec





#### 8.2 Wave soldering (JEDEC22A111 compliant)

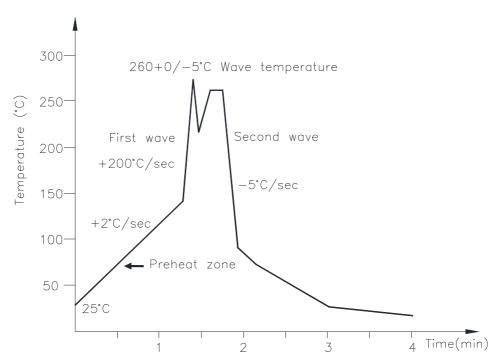
One time soldering is recommended within the condition of temperature.

Temperature: 260+0/-5°C

Time: 10 sec.

Preheat temperature:25 to 140°C

Preheat time: 30 to 80 sec.



#### 8.3 Hand soldering by soldering iron

Allow single lead soldering in every single process. One time soldering is recommended.

Temperature: 380+0/-5°C

Time: 3 sec max.

#### 9. Notes:

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