TOSHIBA Photocoupler GaAs Ired & Photo-Triac

# **TLP762J**

Office Machine
Household Use Equipment
Triac Driver
Solid State Relay

The TOSHIBA TLP762J consists of a GaAs infrared LED optically coupled to a photo-triac in a 6 lead plastic DIP.

- Peak off-state voltage: 600 V (Min.)
- Trigger LED current: 10 mA (Max.)
- On-state current: 100 mA (Max.)
- Isolation voltage: 4000 Vrms (Min.)
- UL recognized: UL1577, file no. E67349
- BSI approved: BS EN60065: 2002,

Certificate No. 8945 BS EN60950-1: 2002, Certificate No. 8946

• SEMKO approved: SS EN60065 (EN60065, 1993)

SS EN60950 (EN60950, 1992) SS EN60335 (EN60335, 1988) Certificate No. 9522145

• Option (D4) type

VDE approved: DIN EN 60747-5-2

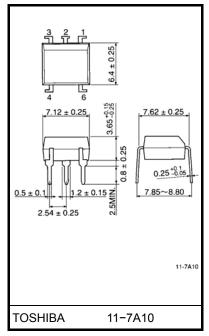
Certificate No. 40009373

Maximum operating insulation voltage:  $890~V_{Pk}$  Highest permissible over voltage:  $6000~V_{Pk}$ 

(Note) When an EN60747-5-2 approved type is needed, please designate the "option (D4)".

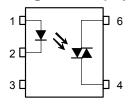
		7.62 mm pich	10.16 mm pich
		TLP762J type	TLP762JF type
•	Creepage distance:	7.0 mm (Min.)	8.0 mm (Min.)
	Clearance:	7.0 mm (Min.)	8.0 mm (Min.)
	Internal creepage path:	4.0 mm (Min.)	4.0 mm (Min.)
	Insulation thickness:	0.5 mm (Min.)	0.5 mm (Min.)

Unit: mm



Weight: 0.42 g (Typ.)

### Pin configuration (top view)



- 1: Anode
- 2 : Cathode
- 3: NC
- 4: Terminal 1
- 6: Terminal 2

### Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit	
	Forward current		lF	50	mA
	Forward current derating (Ta ≥ 53	ΔI <sub>F</sub> / °C	-0.7	mA / °C	
ŒD	Peak forward current (100 µs pulse, 100 pps)		I <sub>FP</sub>	1	Α
	Reverse voltage	V <sub>R</sub>	5	V	
	Junction temperature		Tj	125	°C
	Off-state output terminal voltage	-state output terminal voltage		600	V
	On–state RMS current	Ta = 25°C	I <sub>T(RMS)</sub>	100	mA
_	OII-State RIVIS CUITEIT	Ta = 70°C		50	IIIA
Detector	On–state current derating (Ta ≥ 25	5°C)	ΔI <sub>T</sub> / °C	-1.1	mA / °C
Det	Peak on-state current (100µs pulse, 120 pps)		I <sub>TP</sub>	2	Α
	Peak nonrepetitive surge current (PW = 10 ms, DC = 10%)	I <sub>TSM</sub>	1.2	А	
	Junction temperature	Tj	115	°C	
Storage temperature range			T <sub>stg</sub>	-55~125	°C
Operating temperature range			T <sub>opr</sub>	-40~100	°C
Lead soldering temperature (10 s)			T <sub>sol</sub>	260	°C
Isolatio	Isolation voltage (AC, 1 min., R.H.≤ 60%)			4000	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## **Recommended Operating Conditions**

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V <sub>AC</sub>	_	_	240	V <sub>ac</sub>
Forward current	lF	15	20	25	mA
Peak on-state current	I <sub>TP</sub>	_	_	1	Α
Operating temperature	T <sub>opr</sub>	-25	_	85	°C

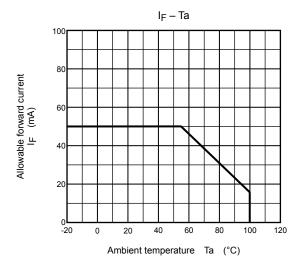
Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

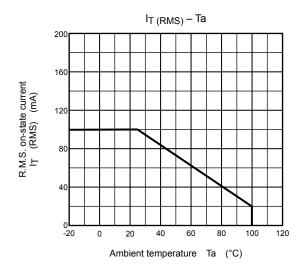
# Individual Electrical Characteristics (Ta = 25°C)

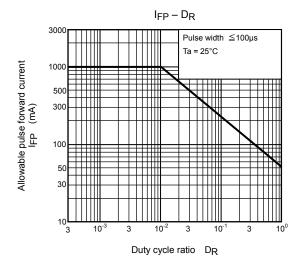
	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
LED	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	_	_	10	μΑ
	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz	_	30	_	pF
	Peak off-state current	I <sub>DRM</sub>	V <sub>DRM</sub> = 600 V	_	10	1000	nA
	Peak on-state voltage	V <sub>TM</sub>	I <sub>TM</sub> = 100 mA	_	1.7	3.0	٧
ctor	Holding current	lΗ	_	_	0.6	_	mA
Detector	Critical rate of rise of off–state voltage	dv / dt	Vin = 240 V, Ta = 85°C	_	500	_	V / µs
	Critical rate of rise of commutating voltage	dv / dt (c)	Vin = 60 Vrms , I <sub>T</sub> = 15 mA	_	0.2	_	V / µs

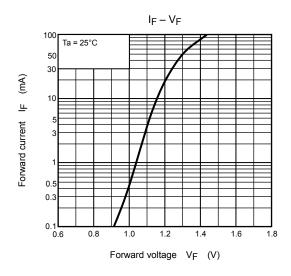
# **Coupled Electrical Characteristics (Ta = 25°C)**

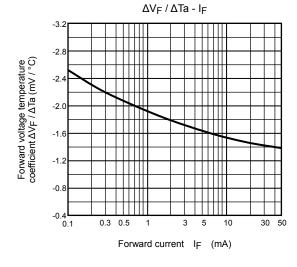
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I <sub>FT</sub>	V <sub>T</sub> = 6 V	_	_	10	mA
Capacitance (input to output)	CS	V <sub>S</sub> = 0, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V	1×10 <sup>12</sup>	10 <sup>14</sup>	_	Ω
	BVS	AC, 1 minute	4000	_	_	Vrms
Isolation voltage		AC, 1 second, in oil	_	10000	_	
		DC, 1 minute, in oil	_	10000	_	V <sub>dc</sub>

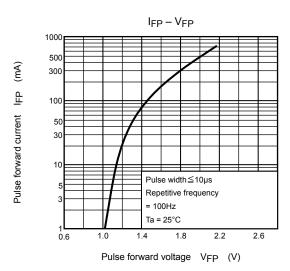












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