#### Features

#### **General Description**

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- , construction of the
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- ≻ •••••• . . . , , , J 1 - 113 ,-**.** ,
- 1.1 2 1 - -1.1.1.1 11 **-**11 -11 -1
- 4

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-

Gas Gauge IC With SMBus Interface

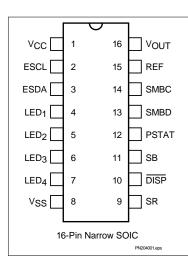
'(΄. ) . . . . . · · · · . . . . . . · , · ... , .... - .... ·· ·· ·· ·· 11 -11 -1 - 110 1 - -•, , • · / / · 

1 11 - 11 . . · , <u>.</u> ~ • ۲  $\sim$ 1. 11 · • • • 1 141 3 .

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Proversion -- 1. The second ·, · ····

#### **Pin Connections**



### **Pin Names**

- $V_{CC}$ 3.0-6.5V EEPROM clock
- ESCL EEPROM data ESDA
- LED segment 1-4 LED<sub>1-4</sub>
- V<sub>SS</sub> System ground
- SR Sense resistor input
- DISP Display control input

- SB Battery sense input PSTAT Protector status input
- SMBus data input/output SMBD
- SMBC SMBus clock
- REF Voltage reference output
- VOUT EEPROM supply output

SLUS005-JUNE 1999 E

#### **Pin Descriptions**

Vcc	Supply voltage input	
ESCL	Serial memory clock	
	, , , , , , , , , , , , , , , , , , ,	SB
ESDA	Serial memory data and address	
	A set of the product of the prod	
LED <sub>1</sub> – LED <sub>4</sub>	LED display segment outputs	PS
$\mathbf{v}_{\mathbf{ss}}$	Ground	
$\mathbf{SR}$	Sense resistor input	SM

 $\begin{bmatrix} 1 & 1 & 1 & 1 \\ 1 & 1 & 1 & 2 \end{bmatrix} = \begin{bmatrix} 1 & 1 & 1 & 2 \\ 2 & 1 & 2 \end{bmatrix} = \begin{bmatrix} 1 & 1 & 2 \\ 2 & 1 \end{bmatrix} = \begin{bmatrix} 1 & 2 \\ 2 & 2 \end{bmatrix} = \begin{bmatrix} 1 & 2 \\ 2$ 

#### **DISP** Display control input

#### Secondary battery input

#### PSTAT Protector status input

And the statement of the second statement of the secon

#### SMBD SMBus data

#### SMBC SMBus clock

#### **REF** Reference output for regulator

→ Jost Jost - Statisty Maryon yours - Jack
 → The Statisty Maryon yours

#### VOUT Supply output

with the second company and the traje

# scription

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►, , · ·

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C5

TP6 ≻—

# bq2040

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0 -0

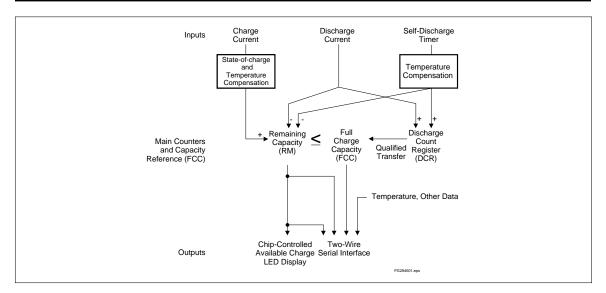
| | | R18

Parameter Name	Address	Description	Length	Units
	<b>.</b>	and place and according of	12	• ۲
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P 101 T 117075	¥~ ¥	that we could a second	12	1
e y <sup>an</sup> fu mu m <b>e</b> <sup>n</sup> an'u e su	۲-1	leave a set of the set	1.7	۲ ۲
	¥ ~ 1	the math math the second	1.5	۲ ۲
	, -,	( , )	- 1-	₹ ►
Trans in 11	<b>,</b> / <b>,</b> ,	the mass of person of a more than the	× 12	• ۲
· cale	, -,	• • • • • •	× 12	۲ ۲
	<b>,</b> / <b>,</b>	in a construction of a construction of a construction of the const	×	۲ ۲
and a second			× 15	1
and a second and a			S 15	۲ ۲
State State State	, / , \		× 15	►
$\Delta_{-}$	<b>.</b> .	$c_{n-n} \in \Delta_{n-n} (x_{n-n}) \in \Delta_{n-n} (x_{n-n}) = \sum_{i=1}^{n-1} (x_{n-n}) = \sum_{i=1}^{n-1} (x_{n-n}) (x_{n-n}) = \sum_{i=1}^{n-1} (x_{n-n}) (x_{n-n}) = \sum_{i=1}^{n-1} (x_{n-n}) = \sum_{i=1}^{n-1} (x_{n-n}) (x$	12	< ►
· ····································		$\Delta \Delta \Delta = $	1.7	◄ ٦
	Y		1.7	۲ ۲
· r estra	· · ·	· · ···		۲ ۲

## Table 1. Configuration Memory Map (Continued)

### Voltage Thresholds

A Constant Constant and Constant A Constant Sector Sector





#### 2. DesignCapacity (DC):

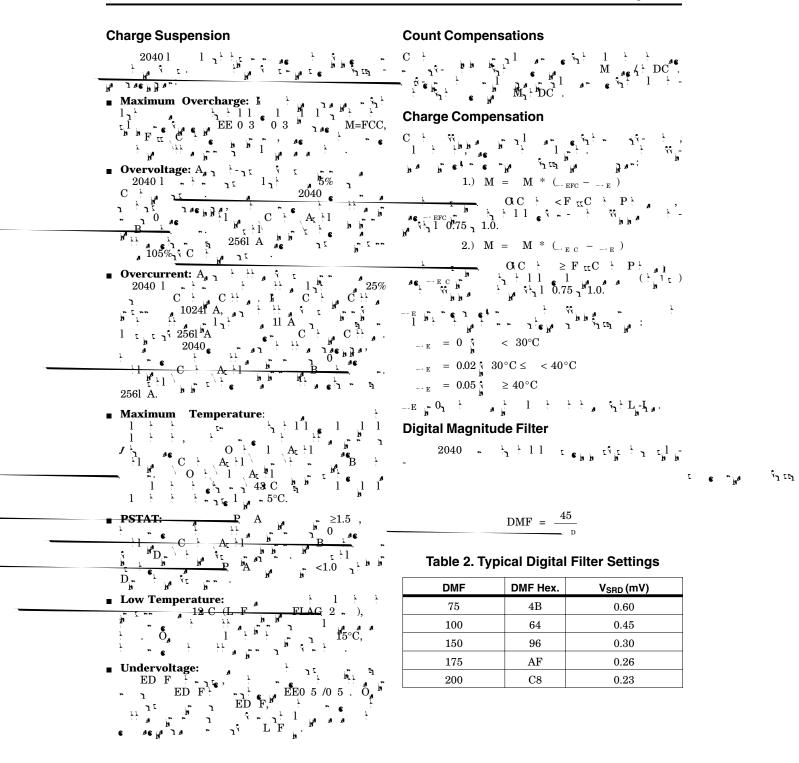
#### 3. RemainingCapacity (RM):

#### 4. Discharge Count Register (DCR):

- The second se
- A state of the second se
- I should be assessed and an and the second secon
- A most termine property and the main state of the second state

## **Charge Counting**

Change, State (1) March (1) H. . . A valid



### **Error Summary**

#### **Capacity Inaccurate**

#### **Current-Sensing Error**

#### Display

 $= -r_1 - \frac{1}{2} \int \frac{1}{r_1} \frac{1}{r_2} \frac{1}{r_1} \frac{1}{r_2} \frac{1}{r_2} \frac{1}{r_1} \frac{1}{r_2} \frac{1}$ 

 $= \sum_{i=1}^{n} \sum_{j=1}^{n} \sum_{i=1}^{n} \sum$ 

#### • • • • • • •

 Constraints of the constraint of th I Start Contract Start Start In The Start Start

## Host-to-bq2040 Messages (see Table 4)

#### ManufacturerAccess() (0x00)

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 $\frac{1}{2} = \frac{1}{2} = \frac{1$ 

 $\begin{array}{c} \mathbf{A}_{1}, \mathbf{a}_{1}, \mathbf{a}_{2}, \mathbf{a}_{3}, \mathbf{a}_{1}, \mathbf{a}_{1}, \mathbf{a}_{2}, \mathbf{a}_{3}, \mathbf{a}_{3},$ 1 1 1 2 1

• ,• 1 -- , •

#### RemainingTimeAlarm() (0x02)

Aryal , a gara for a marker in the second of the second of

#### BatteryMode() (0x03)

Function	Code	Access	Units	Defaults <sup>1</sup>
The construction of the co	Y	se Jage	-	-
• C THE C THE PER	<b>,</b> `	se la spe	►	- `
• • • • • • • •	• •	se Jage	11.5.1	- `
A more T is or	<b>v</b>	se lasse	10.55	-
▶	¥ -	se la spe	►	-
▶	<b>v</b>	· · ·	11.5.1	-
	<b>v</b>	×	11.9.5	-
▶	, ,		A	-
· , / · ~ · ·		5 ×	> <del>-</del>	~
The production of the second sec		· · ·	۲	- `
· • • • • • • • • • • • • • • • • • • •		· · ·	•	
Mar + 17 garden	v		•	
Y	· · ·	· · •	,	×
· it all a star so it	, -	· · .	,	-
<ul> <li>A stand of the sta</li></ul>	· · · ·	· · ·	,	-
· · · · · · · · · · · · · · ·	<b>,</b> ,	· · •	•	- `
175 16 1 1 1 m	<b>v</b>	· · •	•	-
• • • • • • • • • •	<b>v</b> <sup>1,1</sup>	· · •	11 9 - 1	-
No	<b>v</b> <sup>N</sup> ~	· · •	11	-
No. c. c	<b></b> *	· · •	11	-
e sener asser	<b>v</b> -	· · ·	•	- `
e in the second	<b>,</b> `	3 A 💊	۲	
k nnti n ny	<b></b>	3 A 💊	10.50	- `
e 1770 - 110	<b>, , , , , , , , , ,</b>			- `
1 P 10 1 110	<b></b>	3 A 💊	►	- `
r p , tomps	<b>,</b> `	· · ·	7	- `
VIIII MAN	<b></b>	· · •	-	- `
The course of	<b></b>	· · •	-	- `
~, <b>-</b> , ~	× -	· · •	11 - 4 - 13	- `
• rate		-	-	-
The constant	•	5 C 💊	~ <b>.</b>	
· 1	• · · ·		- <sup>5</sup> 11 •	- `

## Table 4. bq2040 Register Functions

Note: N. C., The product of the second secon

Function	Code	Access	Units	Defaults <sup>1</sup>
A 114 1 1 5	۲	· * •	2° 11 🖉	- `
	<b>V</b> *		~ 11 <b>*</b>	-
	Y ~ Y		10.50	-
	• ´		-	-
	<b>v</b>	5 A 🖕	-	-

- a) a construction of the second second
- I a set of a set of the set of

the state of the second second

| 1 1~ ≯ ►

• 1 + 17 = 29 • • • 20 - 29 • • •

#### AtRateTimeToFull() (0x05)

y any she ya the angle the transformed and a t

- 111-2 111-1
- S I T SIN FOR II S Conto

#### AtRateTimeToEmpty() (0x06)

 $\left\{ \begin{array}{cccc} 1 & 2 & - 1 & - 1 & - 2$ 

ny vn na na na transferier na transferier na transferier

- 111-2 111-1
- S I V SIN FO II S CONS

#### AtRateOK() (0x07)

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A server is the set of an end of a set of a set of the set of t

· ,• ·; · , - ≠ **,** ► -

#### Temperature() (0x08)

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- 11.1.2 > -
- s in the terms that
- ▶..., × ..., ± ..., × ..., × ...,

#### Voltage() (0x09)

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- ► regers to \$ %. . . . Tree , and a get any

#### Current() (0x0a)

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- a proper a Mana a spir a spir a sand

#### AverageCurrent() (0x0b)

111-2 1

```
این ادیم و مادی الا اور این اوم و .
مرود مرکز الاردی الا
```

 $\blacktriangleright (a_1, a_2) \pm \% + (a_1, a_2) + (a_2, a_3) + (a_1, a_2) + (a_1, a_2)$ 

#### MaxError() (0x0c)

• • • • %

#### RelativeStateOfCharge() (0x0d)

(%). RelativeStateOfCharge is only valid for battery capacities more than 1504mAh and less than 10,400mAh.

#### AbsoluteStateOfCharge() (0x0e)

(%). (%). Absolute StateOfCharge is only valid for battery capacities more than 1504mAh and less than 10,400mAh.

- , , , %
- ,• '; , %
- · · · · · · · · · %

#### RemainingCapacity() (0x0f)

and and an and a second and a

1 1 1 × 1 ►

- s proper the second second

#### FullChargeCapacity() (0x10)

1 1 1 × 2 ►

a proper where a property contraction

#### RunTimeToEmpty() (0x11)

111-2 111-1

- i print a pris carti

4. The second strain of a second seco

#### AverageTimeToEmpty() (0x12)

・マッキ・・1 アイシーバック ペ・・ シッジュート シール・マー・・・ シノペートレアン・

- 111-2 111-1
- Supering a more contra

A the construction of the second seco

#### AverageTimeToFull() (0x13)

111-2 111-5

Survey and some

All the construction of the second construction

#### ChargingCurrent() (0x14)

And the second of the second s

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to the the the the terms of the track of the terms of terms
```

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• ,• 1 ~ , - •

s proper the second of the second

All the end of white the state of the second s

#### ChargingVoltage() (0x15)

An open of the second of the s

```
and a promotion of phase of
```

1 . . . . . . .

s , , r s , a ; . Man a c p , Tora parts canto

A Constant of the constant of

#### BatteryStatus() (0x16)

a sector as security and and and any asset

#### Table 5. Status Register

Alarm Bits							
	And the second s						
¥-							
	• • • • • •						
×	·						
	· · ···						
	• · · · · · · · · · · · · ·						
	• · · · · · · · · · · · · · · · · · · ·						
	Status Bits						
¥	4, 15, 7, 7.						
<b></b> .							
	• , ] , · · • · •						
Error Code							

#### CycleCount() (0x17)

1 1 1 - 7 - 100

Manage and the second se

s in the start of the

## DesignCapacity() (0x18)

and the second of the second o

#### End of Discharge Voltage1 (0x3e)

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ار این از می وارد ایران ایران از دور ایران می وارد ایران می وارد ایران ایران ایران می وارد ایران می وارد ایران
ایران می وارد می وارد ایران می وارد ایران می وارد ایران می وارد می وارد می
```

```
ywyd ach cyfr cyfregy pyrau yn fri
Cryfy - antor cyfrifyr cyfreg cyfrif
pol - cyfrif ( 1997)
```

#### End of Discharge VoltageF (0x3f)

```
a line and a second second
Second second second second second second second second second second second second second second second second
```

```
\begin{array}{c} & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & & \\ & & & \\ & & & \\ & &
```

#### FLAGS1&2() (0x2f)

, ... **>** ...

#### FLAGS2

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- nr 1 - 11
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First End-of-Discharge Voltage, ( TN), A,

## SBD Seal

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## - THE GALS - MALE THE SS T. N. SUTIMAN <u>B</u>E TH

S Trance The part

## **Error Codes and Status Bits**

Error	Code	Access	Description						
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۸ <sub>۷</sub>	• `	an Jan	the second state of the provide state of the provide state of the second state of the						
• • • • • • • • •	• ~	er Jager	e fan en ele gegenne fer oar strok fer fer en en en genege fer neer op gegen. Ferster						
	۲	ar Jagor	and the second state of the second states and the second states an						
►	•	1.1.1	n an an air an an ann an ann an ann an an an an an						
Aron / John		ar Jagar	the contract of the second of the second						
• •	•	1.1.1	<pre>/ * * * * * * * * * * * * * * * * * * *</pre>						
· , · , _ = · · · ·	• •	er Jager							

## Table 8. Error Codes (BatteryStatus() (0x16))

	Alarm Bits			
Bit Name	Set When:	Reset When:		
Ţ], ▶]▶ ▶.Ţ	$\Delta /\Delta$ . (Note: $\Delta T/\Delta t$ and current taper are valid charge terminations.)	$ \begin{array}{c} \bullet & \bullet \\ \Delta & \Delta_{a,a,a} & \bullet \\ \bullet & \bullet \\ \Delta & \Delta_{a,a,a} & \bullet \\ \bullet & \bullet $		
	$\Delta = \left\{ \begin{array}{c} \Delta & \Delta \\ $			
7]_7 /]► ►.7				
		- 1.5 (5 - 1.5) - 4.5 (5 - 5.5		
7.4 4[_X [ X.4 7	<pre></pre>	- John South Angel (South South Angel (South South South Angel (South South So		
	Status Bits			
Bit Name	Set When:	Reset When:		
<i>₹</i> ↓ ↓ ↓ _				
⊥, ▶. J		<ul> <li>and states a signal date</li> <li>contraction and signal date</li> </ul>		
-, l, ▶				
] 4,	n an	• · · · · · · · · · · · · · · · · · · ·		

		ROM ress		ROM ontents		
Description	Low Byte	High Byte	Low Byte	High Byte	Example Values	Notes
- /. T	v		-		×	·
- 1. 1	• `				~	Γ
• • • • • • •	γ	۲			×	the second se
• • • • • • •	۲ -	•	,			en ners e, eps recorder.
• • • • • •	Y	• •				Transford and the second
4, 10, 77 Se 110	۲	۲			•	the manufactory of the second second
in the line of	•	•	-	-	\ <b>т</b>	the section of a sector of the
•	, <i>′</i>				Χ.,	A ja f A marts a my .
· · · · · · · · · · · · · · · · · · ·	۰ ۲	• •				and the second sec
· • · · · · · ·	Ň	•××			<b>&gt;</b>	The mater process of the the
C. P. Samer	× -	Ň	-		N T	The methy colorador
	¥ -	¥	~		۲.	and according to a second
i ya rayar	×	¥ 1	~	~	Γ, ,	Constant in the second
···· ·	×	Ň	N.,		Χ.,	en and a construction of the construction
nt sente Cassel n	Ý	Ý				the second of the second se
	¥ *	÷ -			►	
• • • • •	× -	÷.				The matter of the second se
dan .	•**	<b>y</b> ~ <b>~</b>	,	· · ·	•	en al or en el se el ser a la ser a contra se contra se a contra se contra se a contra se a contra se
•	•**				%	(*************************************

## Table 10. Example Register Contents

		ROM ress	He	PROM Hex htents		
Description	Low Byte	High Byte	Low Byte		Example Values	Notes
• • • • • •	Y . 1					The state of the second state of the second
1-4 1 - 2 - C - 1 - 2 - C	•	•	× .		►	the material of the second sec
	•	•	,	.,	× ►	and the second sec
· · · · · ·	<b>,</b> ´					
► eee = y sometime	v -				• ···· · · · · · ·	A mer en a ser en por por an anti-por por en
·	• ′					
• • .	¥ `					ζ <sub>121</sub> τ <sub>1</sub> τ ▼ ↓
	٧	٧- *		,	-	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$
A more to make a	۲.				-, 1	$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix}$
·	٧				۰.°	
	۲.		``			- (
( )) ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( )	₹		``			$ \begin{bmatrix} c_{1} & c_{2} & c_{3} & c$
	¥- 1		,		× %	and the second s

## Table 10. Example Register Contents (Continued)

Note: Note: Note: The second state of the seco

String Description	Address	0x X0	0x X1	0x X2	0x X3	0x X4	0x X5	0x X6	0x X7	0x X8	0x X9	0x Xa	0x Xb
· · · · · · · · · · · ·	¥* - ¥*			-	ſ	-	-	Ţ	.S ►	•	х -	-	-
A.,, .	• - • -		•	۲. -			-		-				
A present as	v v -	-	~	1	- 1	ĩ	-						
ly logili e o	• - •		•	<u>к</u> -	-								

## Absolute Maximum Ratings

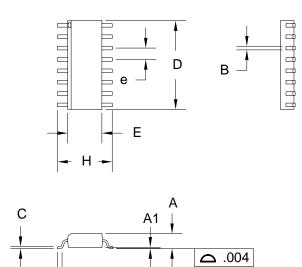
Symbol	Parameter	Minimum	Maximum	Unit	Notes
۲	• ~		+, .	۲	
Press Provid	· real rant		+, .	۲	
• - ¬	· cr all cast		+ .	۲	· · · · · · · · · · · · · · · · · · ·
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## **Recommended DC Operating Conditions** (TA = TOPR)

## 16-Pin SOIC Narrow (SN)

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## 16-Pin SN (SOIC Narrow)

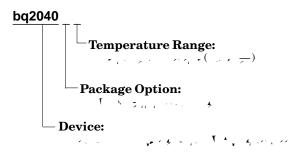
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## **Data Sheet Revision History**

Notes:

## **Ordering Information**



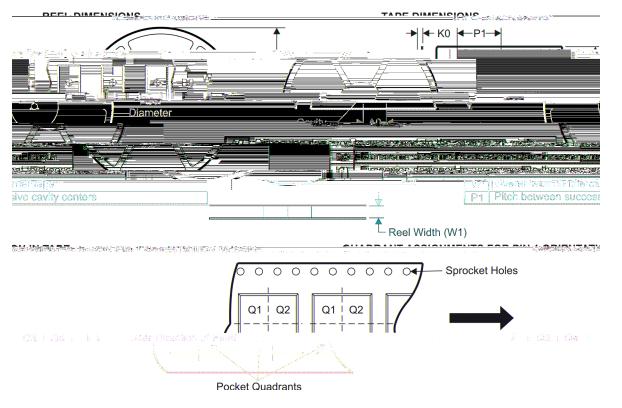
## **PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type		

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## TAPE AND REEL INFORMATION



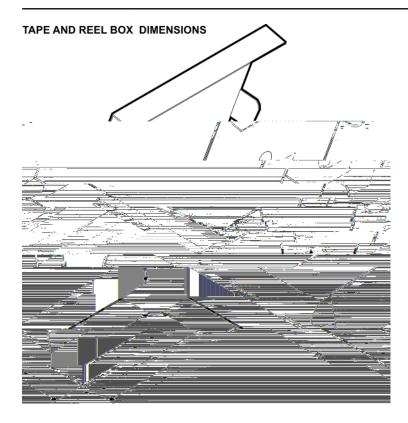
Device	-	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
BQ2040SN-C408TR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
BQ2040SN-D111TR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1

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## PACKAGE MATERIALS INFORMATION

22-Sep-2009



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
BQ2040SN-C408TR	SOIC	D	16	2500	346.0	346.0	33.0
BQ2040SN-D111TR	SOIC	D	16	2500	346.0	346.0	33.0

### **IMPORTANT NOTICE**

Texas Instruments Incorporated