

# bq20z90

SLUS778-JULY 2007

# SBS 1.1-COMPLIANT GAS GAUGE ENABLED WITH IMPEDANCE TRACK™ TECHNOLOGY FOR USE WITH THE bq29330

## FEATURES

- Patented Impedance Track<sup>™</sup> Technology Accurately Measures Available Charge in Li-Ion and Li-Polymer Batteries
- Better than 1% Error Over Lifetime of the Battery
- Instant Accuracy No Learning Cycle Required
- Automatically adjusts for battery aging, battery self discharge and temperature inefficiencies
- Supports the Smart Battery Specification SBS V1.1
- Works With the TI bq29330 Analog Front-End (AFE) Protection IC to Provide Complete Pack Electronics Solution
- Full Array of Programmable Voltage, Current, and Temperature Protection Features
- Integrated Time Base Removes Need for External Crystal with Optional Crystal Input
- Electronics for 7.2-V, 10.8-V or 14.4-V Battery Packs With 50% Fewer External Components
- Based on a Powerful Low-Power RISC CPU Core With High-Performance Peripherals
- Integrated Field Programmable FLASH

- Lifetime Data Logging
- 30-Pin TSSOP (DBT)

## APPLICATIONS

- Notebook PCs
- Medical and Test Equipment
- Portable Instrumentation

# DESCRIPTION

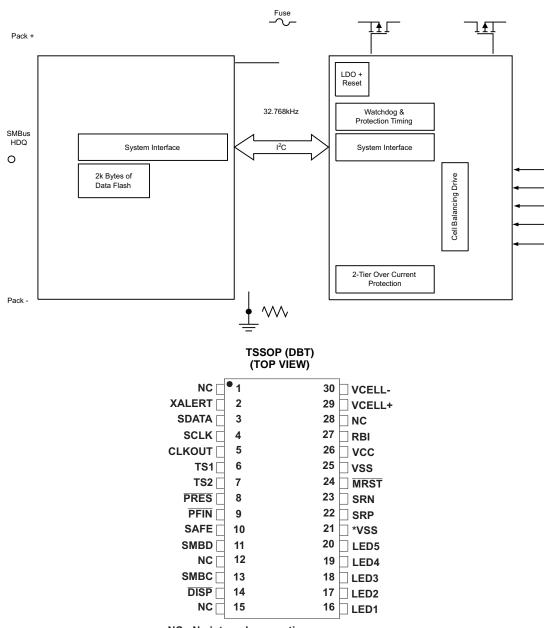
bq20z90 SBS-compliant gas The gauge IC, Track™ patented Impedance incorporating technology, is designed for battery-pack or in-system installation. The bg20z90 measures and maintains an accurate record of available charge in Li-ion or Li-polvmer batteries using its integrated high-performance analog peripherals. The bg20z90 monitors capacity change, battery impedance, open-circuit voltage, and other critical parameters of the battery pack, and reports the information to the system host controller over a serial-communication bus. It is designed to work with the bg29330 analog front-end (AFE) protection IC to maximize functionality and safety, and minimize component count and cost in smart battery circuits.

The Impedance Track technology continuously analyzes the battery impedance, resulting in superior gas-gauging accuracy. This enables remaining

Discharge / Charge / Pre-Charge FETs

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## SYSTEM PARTITIONING DIAGRAM



NC - No internal connection

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### **TERMINAL FUNCTIONS**

| TE  | RMINAL | 1/O <sup>(1)</sup> | DESCRIPTION   |  |  |  |  |  |
|-----|--------|--------------------|---|--|--|--|--|--|
| NO. | NAME   | 1/0(1)             | DESCRIPTION   |  |  |  |  |  |
| 1   | NC     | _                  | Not used— leave floating  |  |  |  |  |  |
| 2   | XALERT | I                  | Input from bq29330 XALERT output.   |  |  |  |  |  |
| 3   | SDATA  | I/O                | Data transfer to and from bq29330   |  |  |  |  |  |
| 4   | SCLK   | I/O                | Communication clock to the bq29330  |  |  |  |  |  |
| 5   | CLKOUT | 0                  | 32.768-kHz output for the bq29330. This pin should be directly connected to the AFE.  |  |  |  |  |  |
| 6   | TS1    | I                  | 1 <sup>st</sup> Thermistor voltage input connection to monitor temperature  |  |  |  |  |  |
| 7   | TS2    | I                  | 2 <sup>nd</sup> Thermistor voltage input connection to monitor temperature  |  |  |  |  |  |
| 8   | PRES   | I                  | Active low input to sense system insertion and typically requires additional ESD protection   |  |  |  |  |  |
| 9   | PFIN   | I                  | Active low input to detect secondary protector output status and allows the bq20z90 to report the status of the 2 <sup>nd</sup> level protection output |  |  |  |  |  |
| 10  | SAFE   | 0                  | Active high output to enforce additional level of safety protection; e.g., fuse blow.   |  |  |  |  |  |
| 11  | SMBD   | I/OD               | SMBus data open-drain bidirectional pin used to transfer address and data to and from the bq20z90   |  |  |  |  |  |
| 12  | NC     | -                  | Not used— leave floating  |  |  |  |  |  |
| 13  | SMBC   | I/OD               | SMBus clock open-drain bidirectional pin used to clock the data transfer to and from the bq20z90  |  |  |  |  |  |
| 14  | DISP   | I                  | Display control for the LEDs. This pin is typically connected to bq29330 REG via a 100-k resistor and a push-button switch to VSS.                      |  |  |  |  |  |
| 15  | NC     | -                  | Not used— leave floating  |  |  |  |  |  |
| 16  | LED1   | 0                  | LED1 display segment that drives an external LED depending on the firmware configuration  |  |  |  |  |  |
| 17  | LED2   | 0                  | LED2 display segment that drives an external LED depending on the firmware configuration  |  |  |  |  |  |
| 18  | LED3   | 0                  | LED3 display segment that drives an external LED depending on the firmware configuration  |  |  |  |  |  |
| 19  | LED4   | 0                  | LED4 display segment that drives an external LED depending on the firmware configuration  |  |  |  |  |  |
| 20  | LED5   | 0                  | LED5 display segment that drives an external LED depending on the firmware configuration  |  |  |  |  |  |
| 21  | VSS    | -                  | Connected I/O pin to VSS  |  |  |  |  |  |
| 22  | SRP    | IA                 | Connections to the top of a small-value sense resistor to monitor the battery charge- and discharge-current flow  |  |  |  |  |  |
| 23  | SRN    | IA                 | Connections to the bottom of a small-value sense resistor to monitor the battery charge- and discharge-current flow                                     |  |  |  |  |  |
| 24  | MRST   | I                  | Master reset input that forces the device into reset when held low. Must be held high for normal operation  |  |  |  |  |  |
| 25  | VSS    | Р                  | Negative Supply Voltage   |  |  |  |  |  |
| 26  | VCC    | Р                  | Positive Supply Voltage   |  |  |  |  |  |
| 27  | RBI    | Р                  | Backup power to the bq20z90 data registers during periods of low operating voltage. RBI accepts a storage capacitor or a battery input.                 |  |  |  |  |  |
| 28  | NC     | -                  | Not used— leave floating  |  |  |  |  |  |
| 29  | VCELL+ | I                  | Input from bq29330 used to read a scaled value of individual cell voltages  |  |  |  |  |  |
| 30  | VCELL- | I                  | Input from bg29330 used to read a scaled value of individual cell voltages  |  |  |  |  |  |

(1) I = Input, IA = Analog input, I/O = Input/output, I/OD = Input/Open-drain output, O = Output, OA = Analog output, P = Power



## **ABSOLUTE MAXIMUM RATINGS**

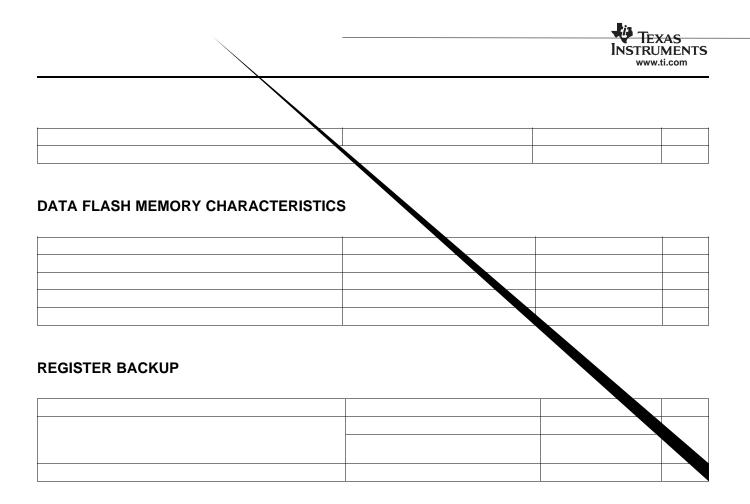
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# **POWER-ON RESET**

# INTEGRATING ADC (Coulomb Counter) CHARACTERISTICS

# OSCILLATOR



# **SMBus TIMING SPECIFICATIONS**

 $V_{CC}$  = 2.4 V to 2.6 V,  $T_{A}$  = –40°C to 85°C (unless otherwise noted)

| 00                    |   | ,                                      |     |      |     |      |  |
|-----------------------|---|--|-----|------|-----|------|--|
|                       | PARAMETER                               | TEST CONDITIONS                        | MIN | TYP  | MAX | UNIT |  |
| f <sub>SMB</sub>      | SMBus operating frequency               | Slave mode, SMBC 50% duty cycle        | 10  |      | 100 |      |  |
| f <sub>MAS</sub>      | SMBus master clock frequency            | Master mode, no clock low slave extend |     | 51.2 |     | kHz  |  |
| t <sub>BUF</sub>      | Bus free time between start and stop    |  | 4.7 |      |     |      |  |
| t <sub>HD:STA</sub>   | Hold time after (repeated) start        |  | 4   |      |     |      |  |
| t <sub>SU:STA</sub>   | Repeated start setup time               |  | 4.7 |      |     | S    |  |
| t <sub>SU:STO</sub>   | Stop setup time                         |  | 4   |      |     |      |  |
|                       | Data hald time                          | Receive mode                           | 0   |      |     |      |  |
| t <sub>HD:DAT</sub>   | Data hold time                          | Transmit mode                          | 300 |      |     | ns   |  |
| t <sub>SU:DAT</sub>   | Data setup time                         |  | 250 |      |     |      |  |
| t <sub>TIMEOUT</sub>  | Error signal/detect                     | See <sup>(1)</sup>                     | 25  |      | 35  | ms   |  |
| t <sub>LOW</sub>      | Clock low period                        |  | 4.7 |      |     | -    |  |
| t <sub>HIGH</sub>     | Clock high period                       | See <sup>(2)</sup>                     | 4   |      | 50  | S    |  |
| t <sub>LOW:SEXT</sub> | Cumulative clock low slave extend time  | See <sup>(3)</sup>                     |     |      | 25  |      |  |
| t <sub>LOW:MEXT</sub> | Cumulative clock low master extend time | See <sup>(4)</sup>                     |     |      | 10  | ms   |  |
| t <sub>F</sub>        |   |  |     |      |     |      |  |
|                       |   |  |     |      |     |      |  |



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## FEATURE SET

## Primary (1st Level) Safety Features

The bq20z90 supports a wide range of battery and system protection features that can easily be configured. The primary safety features include:

- Battery cell over/under voltage protection
- Battery pack over/under voltage protection
- 2 independent charge overcurrent protection
- 3 independent discharge overcurrent protection
- Short circuit protection
- Over temperature protection
- AFE Watchdog
- Host Watchdog

## Secondary (2nd Level) Safety Features

The secondary safety features of the bq20z90 can be used to indicate more serious faults via the SAFE (pin 10) pin. This pin can be used to blow an in-line fuse to permanently disable the battery pack from charging or discharging. The secondary safety features include:

- Safety over voltage
- Battery cell imbalance
- 2nd level protection IC input
- Safety over current
- Safety over temperature
- Open thermistor
- Charge FET and Zero-Volt Charge FET fault
- Discharge FET fault
- Fuse blow failure detection
- AFE Communication error
- AFE Verification error
- Internal flash data error

#### Charge Control Features

The bq20z90 charge control features include:

- Report the appropriate charging current needed for constant current charging and the appropriate charging voltage needed for constant voltage charging to a smart charger using SMBus broadcasts.
- Determine the chemical state of charge of each battery cell using Impedance Track<sup>™</sup>. Using cell balancing algorithm, gradually decrease the differences in the cells' state of charge in a fully charged state. This prevents high cells from overcharging, causing excessive degradation and also increases the usable pack energy by preventing early charge termination.
- Support Pre-charging/Zero-volt charging
- Support Fast charging
- Support Pulse charging
- Support Charge Inhibit and Charge Suspend modes
- Report charging faults and also indicate charging status via charge and discharge alarms.

### Gas Gauging

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The bq20z90 uses the Impedance Track<sup>™</sup> Technology to measure and calculate the available charge in battery cells. The achievable accuracy is better than 1% error over the lifetime of the battery and there is no full charge-discharge learning cycle required.





See Theory and Implementation of Impedance Track Battery Fuel-Gauging Algorithm application note (SLUA364) for further details.

### LED Display

The bq20z90 can drive a 3-, 4-, or 5- segment LED display for remaining capacity indication. The LED drive current can be adjusted to 3mA, 4mA and 5mA digitally.

## LifeTime Data Logging Features

The bq20z90 offers a lifetime data logging array, where all important measurements are stored for warranty and analysis purposes. The data monitored include:

- Lifetime maximum temperature
- Lifetime minimum temperature
- Lifetime maximum battery cell voltage
- Lifetime minimum battery cell voltage
- Lifetime maximum battery pack voltage
- Lifetime minimum battery pack voltage
- Lifetime maximum charge current
- Lifetime maximum discharge current
- Lifetime maximum charge power
- Lifetime maximum discharge power
- Lifetime maximum average discharge current
- Lifetime maximum average discharge power
- Lifetime average temperature

#### Authentication

The bq20z90 supports authentication by the host using SHA-1.

#### **Power Modes**

The bq20z90 supports 3 different power modes to reduce power consumption:

- In Normal Mode, the bq20z90 performs measurements, calculations, protection decisions, and data updates in 1 second intervals. Between these intervals, the bq20z90 is in a reduced power stage.
- In Sleep Mode, the bq20z90 performs measurements, calculations, protection decisions, 4 Tm /F2 -10 Tf 14.4nts0 1Tj 1 0

#### CONFIGURATION

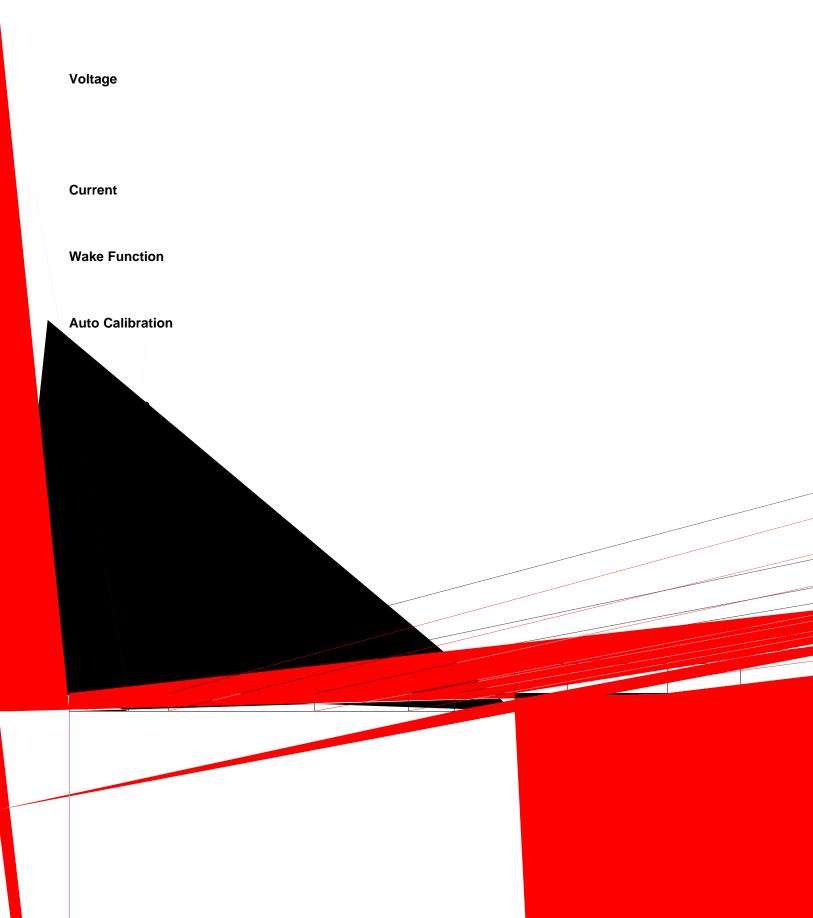
**Oscillator Function** 

**System Present Operation** 

#### **BATTERY PARAMETER MEASUREMENTS**



# Charge and Discharge Counting







|  | I | I |  |  |
|--|---|---|--|--|



## **PACKAGING INFORMATION**

| Orderable Device   | Status <sup>(1)</sup> | Package Type | Package<br>Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup>    | Lead/<br>Ball Finish | MSL Peak Temp <sup>(3)</sup> | Samples<br>(Requires Login) |
|--------------------|-----------------------|--------------|--------------------|------|-------------|----------------------------|----------------------|------------------------------|-----------------------------|
| BQ20Z90DBT-V150    | ACTIVE                | TSSOP        | DBT                | 30   | 60          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-2-260C-1 YEAR          |                             |
| BQ20Z90DBT-V150G4  | ACTIVE                | TSSOP        | DBT                | 30   | 60          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-2-260C-1 YEAR          |                             |
| BQ20Z90DBTR-V150   | ACTIVE                | TSSOP        | DBT                | 30   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-2-260C-1 YEAR          |                             |
| BQ20Z90DBTR-V150G4 | ACTIVE                | TSSOP        | DBT                | 30   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-2-260C-1 YEAR          |                             |

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

#### Important Information and Disclaimer:

#### **IMPORTANT NOTICE**

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