



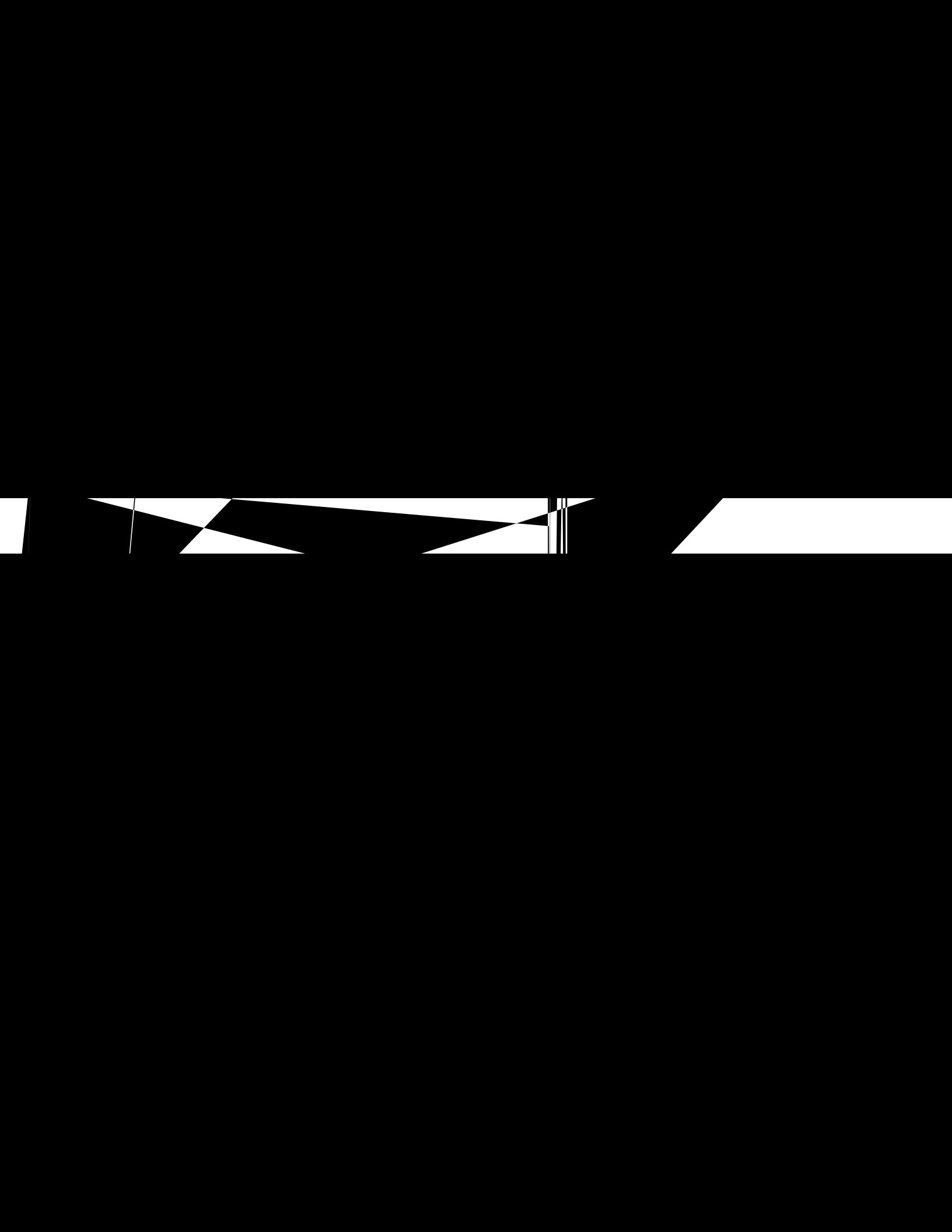
**b 24700, b 24701**  
**NOTEBOOK PC BATTERY CHARGE CONTROLLER**  
**AND SELECTOR WITH DPM**

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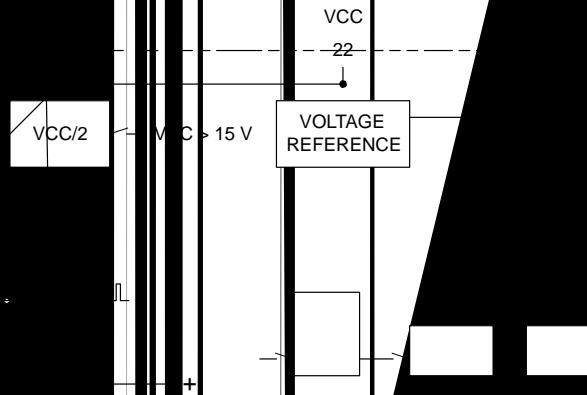
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# CONTROLLER



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**electrical characteristics ( $T_A = T_{OPR}$ , 7.0 Vdc  $V_{CC}$  20.0 Vdc, all voltages relative to  $V_{SS}$ ) (unless otherwise specified) (continued)**

**adapter current-sense amplifier**

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
$g_m$	Transconductance gain		90	150	210	mA/V
CMRR	Common-mode rejection ratio	See Note 1		90		dB
$V_{ICR}$	Common-mode input voltage range (ACP)		7.0	$V_{CC}+0.2$		V
$I_{SINK}$	Sink current (COMP)	COMP = 1 V, (ACP – ACN) = 10 mV	0.5	1.5	2.5	mA
$I_{IB}$	Input bias current (ACP, ACN)	ACP = ACN = 20 V, SRSET = 0 V, VCC = 20 V, ACSET = 1.25 V	15	25	35	$\mu$ A
	Input bias current accuracy ratio (ACP, ACN)	ACP = ACN = 20 V, VCC = 20 V, ACSET = 1.25 V	0.95	1.00	1.05	
$V_{SET}$	AC current programming voltage (ACSET)		0		2.5	V
$A_V$	AC current set gain	$0.65\text{ V} \leq \text{ACSET} \leq 2.5\text{ V}$ , $12\text{ V} \leq \text{ACP} \leq 20\text{ V}$ , $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$ , See Note 4	24.5	25.5	26.5	V/V
	Total ac current-sense mid-scale accuracy	ACSET = 1.25 V, $T_A = 25^\circ\text{C}$ , See Note 5	-5%		5%	
		ACSET = 1.25 V, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$ , See Note 5	-6%		6%	
	Total ac current-sense full-scale accuracy	ACSET = 2.5 V, $T_A = 25^\circ\text{C}$ , See Note 5	-3.5%		3.5%	
		ACSET = 2.5 V, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$ , See Note 5	-4%		4%	

**battery voltage error amplifier**

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
$g_m$	Transconductance gain		75	135	195	mA/V
CMRR	Common-mode rejection ratio	See Note 1		90		dB
$V_{ICR}$	BATSET common-mode input voltage range		1		2.5	V
$V_{IT}$	Internal reference override input threshold voltage		0.20	0.25	0.30	V
$I_{SINK}$	Sink current COMP	COMP = 1 V, (BATP – BATSET) = 10 mV, BATSET = 1.25 V	0.5	1.5	2.5	mA
$V_{FB}$	Error-amplifier precision reference voltage	$T_A = 25^\circ\text{C}$	1.241	1.246	1.251	V
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	1.239	1.246	1.252	
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	1.234	1.246	1.254	

NOTES: 1. Ensured by design. Not production tested.

2.  $I_{BAT} = \frac{SRSET}{R_{SENSE}} \times \frac{1}{A_V}$

3. Total battery-current set is based on the measured value of (SRP–SRN) =

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electrical characteristics ( $T_A = T_{OPR}$ , 7.0 Vdc  $V_{CC}$  20.0 Vdc, all voltages relative to  $V_{SS}$ ) (unless otherwise specified) (continued)

**MOSFET gate drive**

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
AC driver $R_{DS(on)}$ high	$V_{CC} = 18\text{ V}$		150	250	$\Omega$
AC driver $R_{DS(on)}$ low	$V_{CC} = 18\text{ V}$		60	120	$\Omega$
Battery driver $R_{DS(on)}$ high	$V_{CC} = 18\text{ V}$		200	370	$\Omega$
Battery driver $R_{DS(on)}$ low	$V_{CC} = 18\text{ V}$		100	170	$\Omega$
$t_{Da}$ Time delay from ac driver off to battery driver on	ACSEL 2.4 V $\downarrow$ 0.2 V		0.5	1.5	$\mu\text{s}$
$t_{Db}$ Time delay from battery driver off to ac driver on	ACSEL 0.2 V $\uparrow$ 2.4 V		1.0	2.0	$\mu\text{s}$
$V_{OH}$ PWM driver high-level output voltage	$I_{OUT} = -10\text{ mA}$ , $V_{CC} = 18\text{ V}$	-0.12	-0.07		V
	$I_{OUT} = -100\text{ mA}$ , $V_{CC} = 18\text{ V}$	-1.2	-0.7		V

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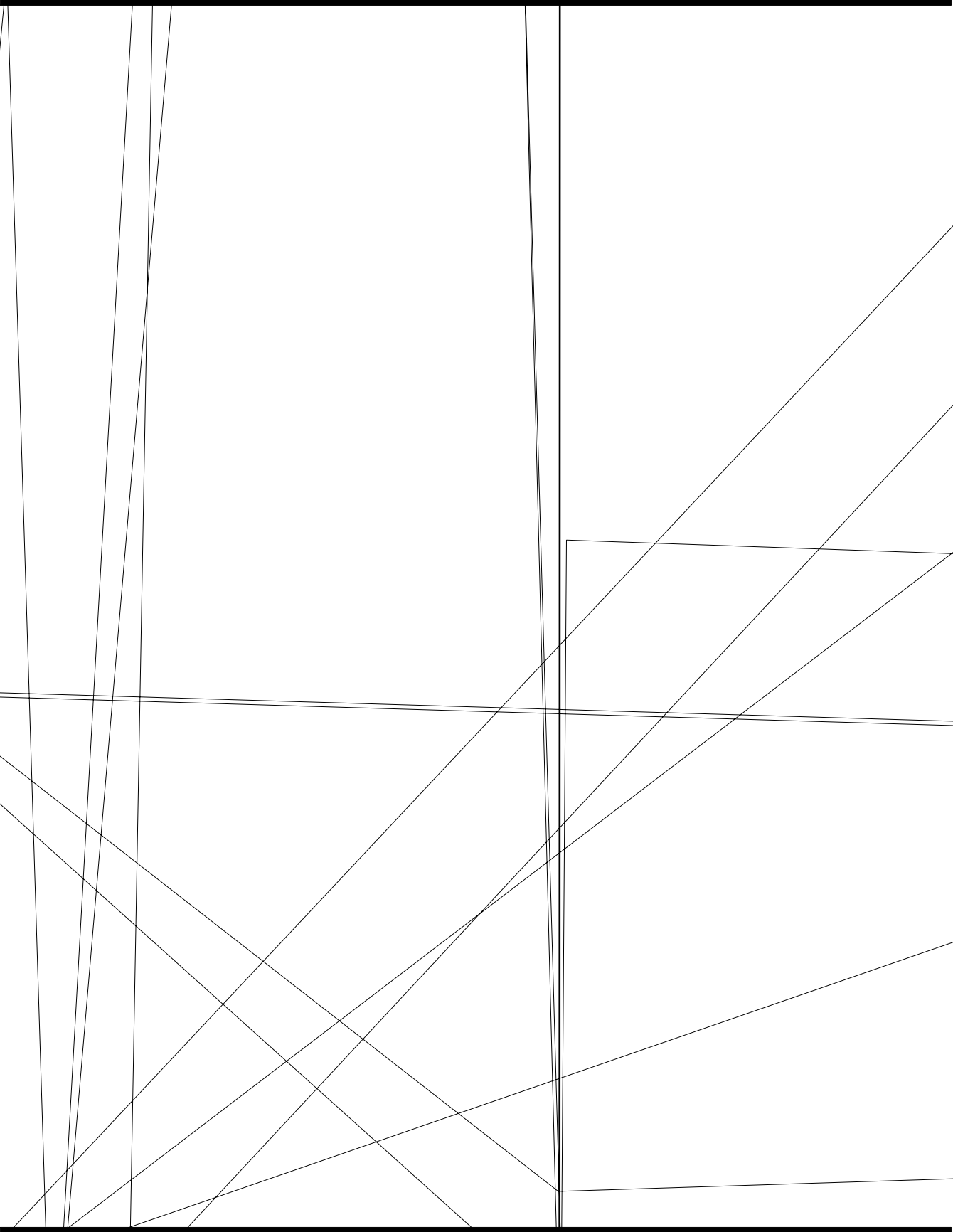
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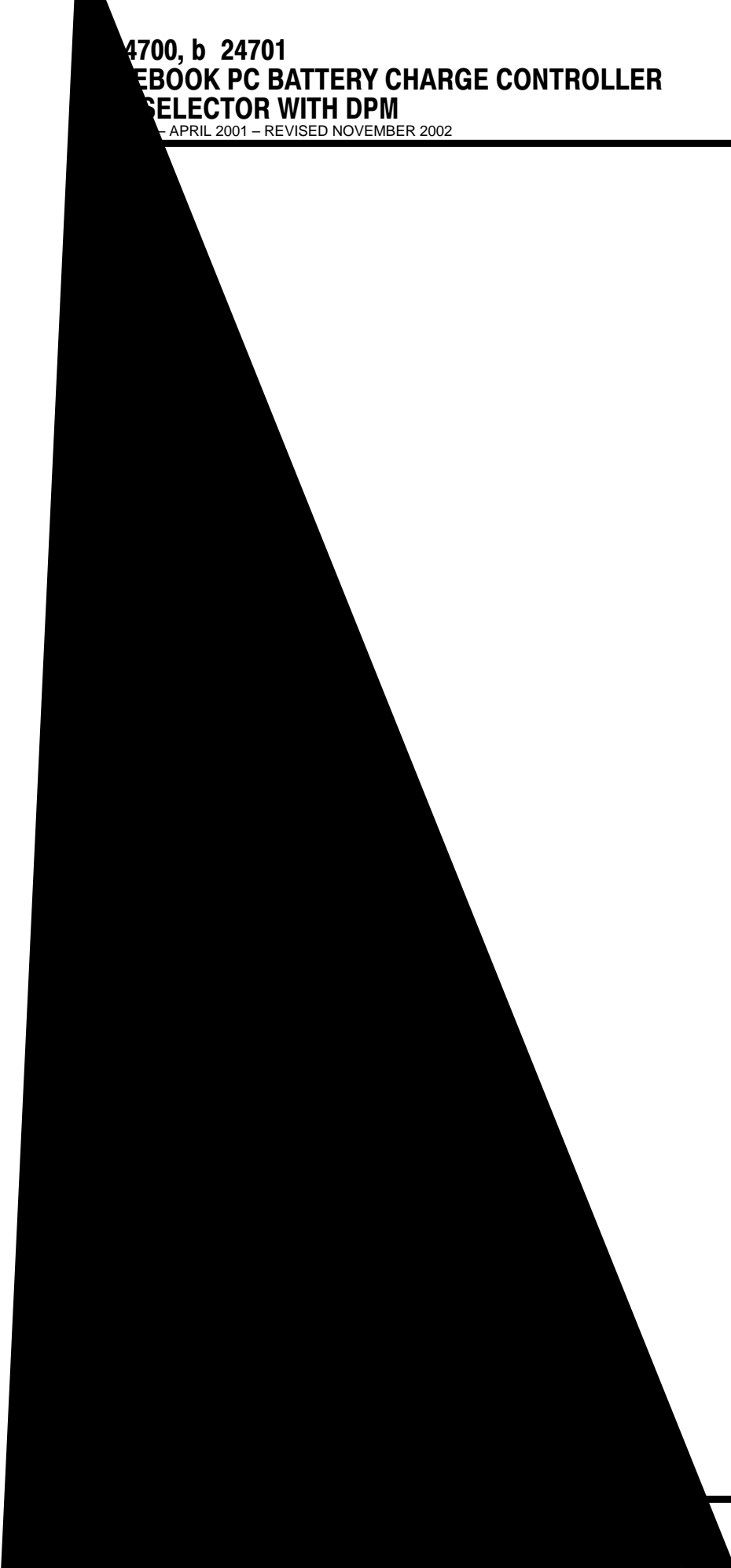
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**APPLICATION INFORMATION**

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**APPLICATION INFORMATION**

The RMS current through the MOSFET is defined as:

$$I_{IN}(RMS) = I_{IN}(avg) \times \sqrt{\frac{1}{D}} A_{RMS}$$

**Schottky rectifier (freewheeling)**

The freewheeling Schottky rectifier must also be selected to withstand the input voltage,  $V_{IN}$ . The average current can be approximated from:

$$I_{D1}(avg) = I_O \times (1 - D) A$$

**choosing an inductance**

Low inductance values result in a steep current ramp or slope. Steeper current slopes result in the converter operating in the discontinuous mode at a higher power level. Steeper current slopes also result in higher output ripple current, which may require a higher number, or more expensive capacitors to filter the higher ripple current.

In addition, the higher ripple current results in an error in the sensed battery current particularly at lower charging currents. It is recommended that the ripple current not exceed 20% to 30% of full scale dc current.

$$L = \frac{(V_{IN} - V_{BAT}) \times V_{BAT}}{f_s \times 0.2 \times I_{FS} \times V_{IN}}$$

Too large an inductor value results in the current waveform of Q1 and D1 in Figure 8 approximating a

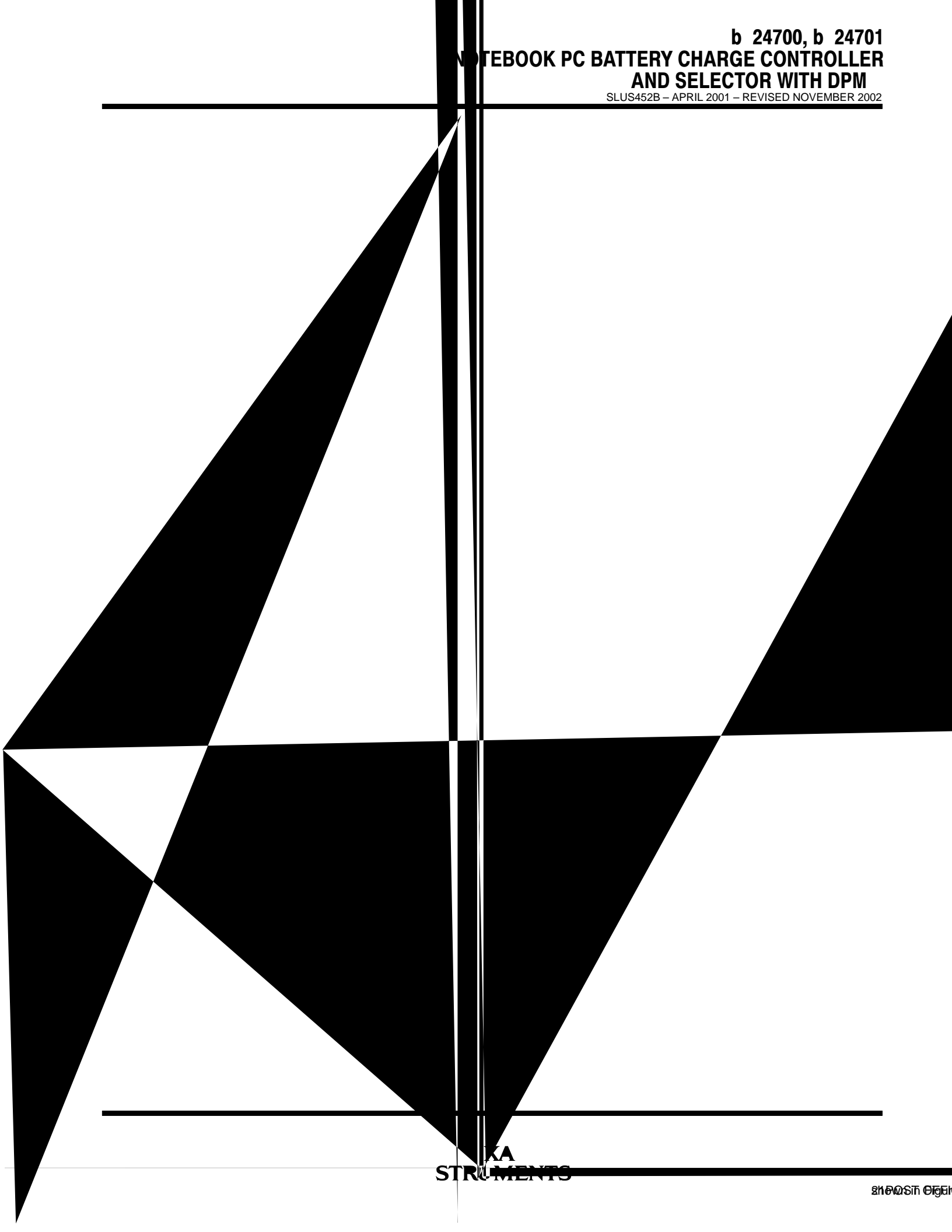
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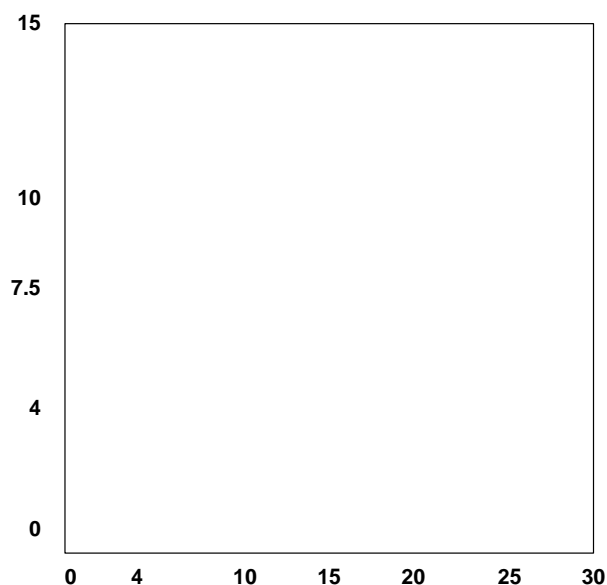
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## APPLICATION INFORMATION

### PWM selector switch gate drive

Because the external P-channel MOSFETs (as well as the internal MOSFETs) have a maximum gate-source voltage limitation of 20 V, the input voltage, VCC, cannot be used directly to drive the MOSFET gate under all input conditions. To provide safe MOSFET-gate-drive at input voltages of less than 20 V, an intermediate gate drive voltage rail was established (VSHP). As shown in Figure 11, VSHP has a stepped profile. For VCC voltages of less than 15 V, VSHP = 0 and the full VCC voltage is used to drive the MOSFET gate. At input voltages of greater than 15 V, VSHP steps to approximately one-half the VCC voltage. This ensures adequate enhancement voltage across all operating conditions.

The gate drive voltage, Vgs, vs VCC for the PWM, and ac selector P-channel MOSFETs are shown in Figure 11.

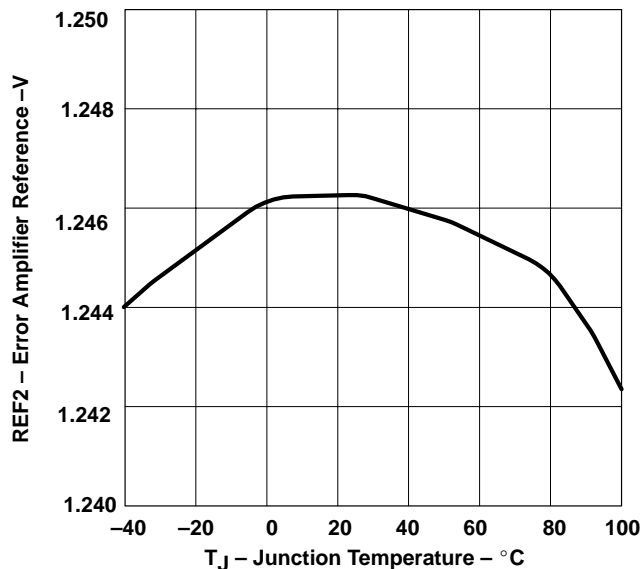


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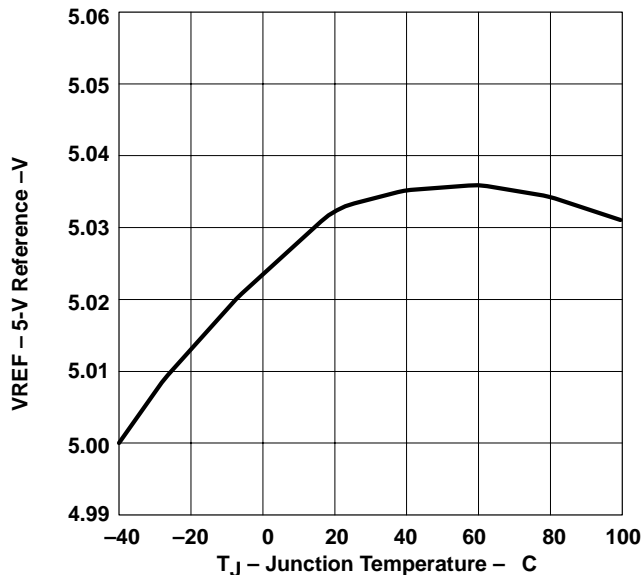
**TYPICAL CHARACTERISTICS**

**ERROR AMPLIFIER REFERENCE**  
**vs**  
**JUNCTION TEMPERATURE**



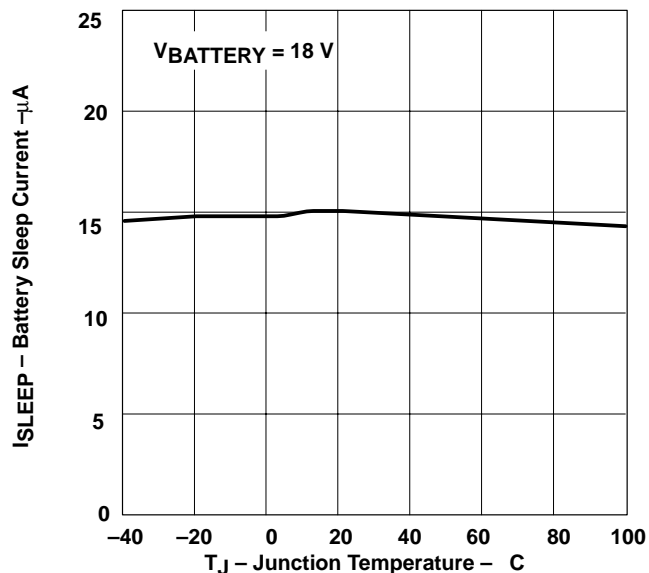
**Figure 12**

**BYPASSED 5-V REFERENCE**  
**vs**  
**JUNCTION TEMPERATURE**



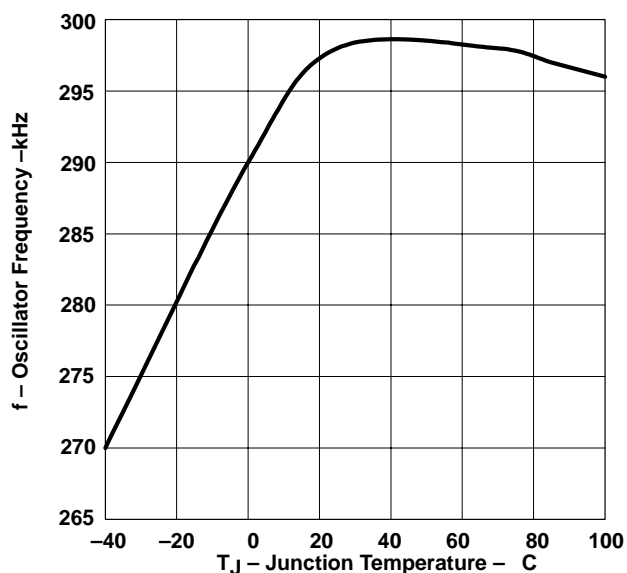
**Figure 13**

**TOTAL SLEEP CURRENT**  
**vs**  
**JUNCTION TEMPERATURE**



**Figure 14**

**OSCILLATOR FREQUENCY**  
**vs**  
**JUNCTION TEMPERATURE**



**Figure 15**



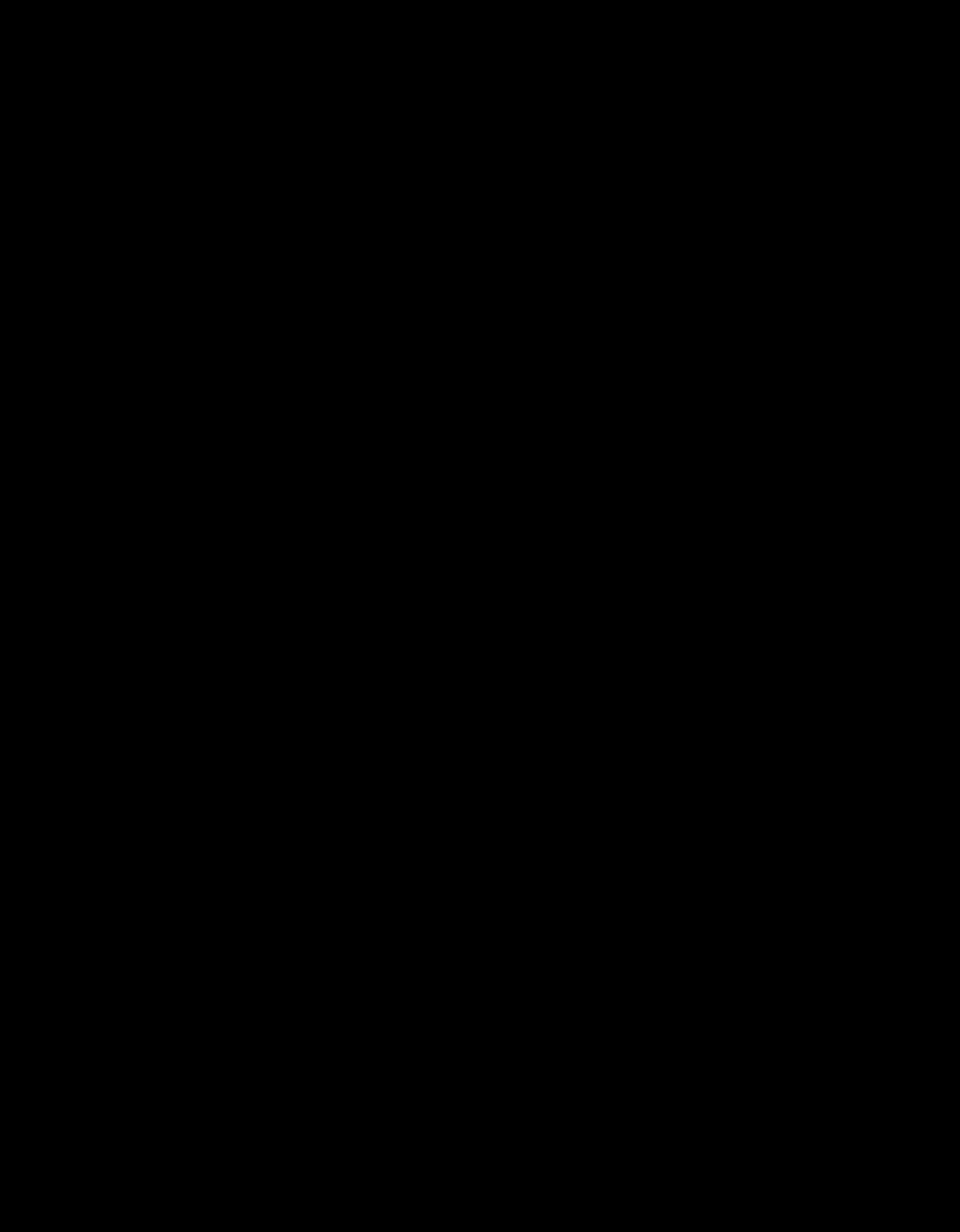
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**PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
BQ24700PW	OBSOLETE	TSSOP	PW	24		TBD	Call TI	Call TI
BQ24700PWR	OBSOLETE	TSSOP	PW	24		TBD	Call TI	Call TI
BQ24700PWRG4	OBSOLETE	TSSOP	PW	24		TBD	Call TI	Call TI
BQ24701PW	OBSOLETE	TSSOP	PW	24		TBD	Call TI	Call TI
BQ24701PWG4	OBSOLETE	TSSOP	PW	24		TBD	Call TI	Call TI
BQ24701PWR	OBSOLETE	TSSOP	PW	24		TBD	Call TI	Call TI
BQ24701PWRG4	OBSOLETE	TSSOP	PW	24		TBD	Call TI	Call TI

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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DLP® Products	<a href="http://www.dlp.com">www.dlp.com</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>
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RF/IF and ZigBee® Solutions	<a href="http://www.ti.com/lprf">www.ti.com/lprf</a>

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Energy and Lighting	<a href="http://www.ti.com/energy">www.ti.com/energy</a>
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