

# C3D04065A-Silicon Carbide Schottky Diode

## Z-Rec<sup>™</sup> Rectifier

$$V_{RRM} = 650 V$$
  
 $I_{F(AVG)} = 4 A$   
 $Q_{c} = 8.5 nC$ 

## Features

- 650-Volt Schottky Rectifier
- Optimized for PFC Boost Diode Application
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V<sub>F</sub>

#### **Benefits**

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

## Applications

- Switch Mode Power Supplies
  - Power Factor Correction - Typical PFC P<sub>out</sub>: 400W-600W

## Package



TO-220-2



Part Numbe	er Package	Marking
C3D04065A	TO-220-2	C3D04065

## **Maximum Ratings**

Symbol	Parameter	Value	Unit	Test Conditions	Note
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage	650	V		
V <sub>RSM</sub>	Surge Peak Reverse Voltage	650	V		
V <sub>DC</sub>	DC Blocking Voltage	650	V		
I <sub>F(AVG)</sub>	Average Forward Current	4	А	T <sub>c</sub> <160°C	
I <sub>frm</sub>	Repetitive Peak Forward Surge Current	22 17	А	$T_c=25$ °C, $t_p=10$ mS, Half Sine Wave D=0.3 $T_c=110$ °C, $t_p=10$ mS, Half Sine Wave D=0.3	
I <sub>fsm</sub>	Non-Repetitive Peak Forward Surge Current	31.9 28.5	А	$T_c=25$ °C, $t_p=10$ mS, Half Sine Wave D=0.3 $T_c=110$ °C, $t_p=10$ mS, Half Sine Wave D=0.3	
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current	110	А	$T_c=25^{\circ}C$ , $t_p=10 \ \mu$ S, Pulse	
P <sub>tot</sub>	Power Dissipation	75 32.5	W	T <sub>c</sub> =25°C T <sub>c</sub> =110°C	
T <sub>J</sub> , T <sub>stg</sub>	Operating Junction and Storage Temperature	-55 to +175	°C		
	TO-220 Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	

1



## **Electrical Characteristics**

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V <sub>F</sub>	Forward Voltage	1.5 1.8	1.8 2.4	V	$I_{F} = 4 A T_{J} = 25^{\circ}C$ $I_{F} = 4 A T_{J} = 175^{\circ}C$	
I <sub>R</sub>	Reverse Current	12 24	60 120	μA	$V_{R} = 650 V T_{J} = 25^{\circ}C$ $V_{R} = 650 V T_{J} = 175^{\circ}C$	
Q <sub>c</sub>	Total Capacitive Charge	8.5		nC	$V_{R} = 650 \text{ V}, I_{F} = 4A$ $di/dt = 500 \text{ A}/\mu\text{s}$ $T_{J} = 25^{\circ}\text{C}$	
С	Total Capacitance	251 22 21		pF	$ \begin{array}{l} V_{_R} = 0 \ V, \ T_{_J} = 25 \ ^\circ C, \ f = 1 \ MHz \\ V_{_R} = 200 \ V, \ T_{_J} = 25 \ ^\circ C, \ f = 1 \ MHz \\ V_{_R} = 400 \ V, \ T_{_J} = 25 \ ^\circ C, \ f = 1 \ MHz \end{array} $	

Note:

1. This is a majority carrier diode, so there is no reverse recovery charge.

#### **Thermal Characteristics**

Symbo	Parameter	Тур.	Unit
R <sub>ejc</sub>	TO-220 Package Thermal Resistance from Junction to Case	2.02	°C/W

## **Typical Performance**

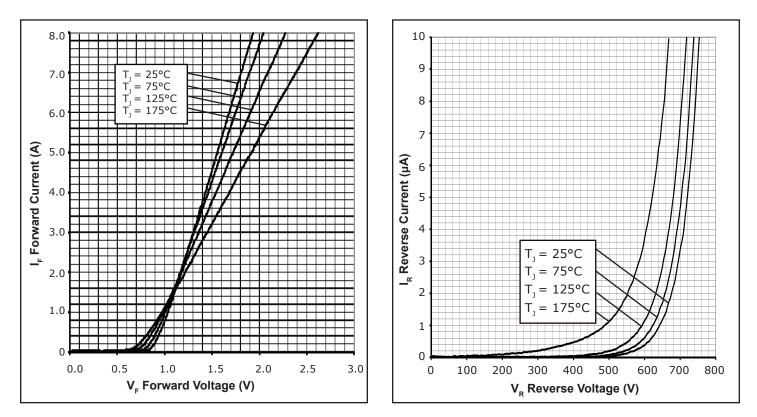


Figure 1. Forward Characteristics





## **Typical Performance**

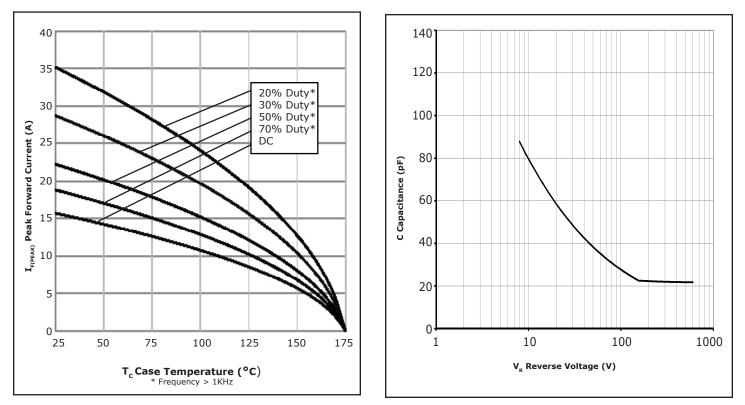


Figure 3. Current Derating

Figure 4. Capacitance vs. Reverse Voltage

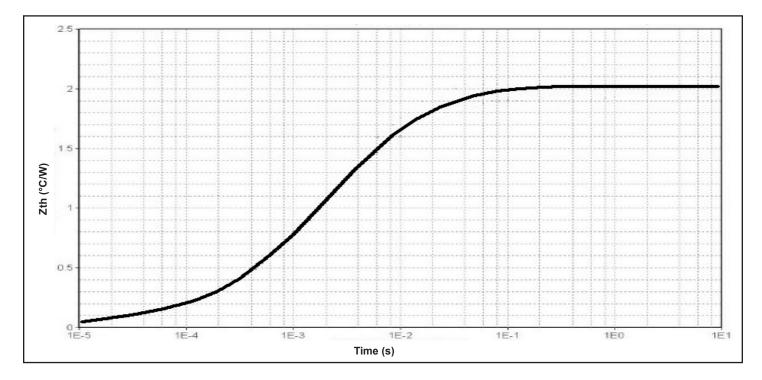


Figure 5. Transient Thermal Impedance



## **Typical Performance**

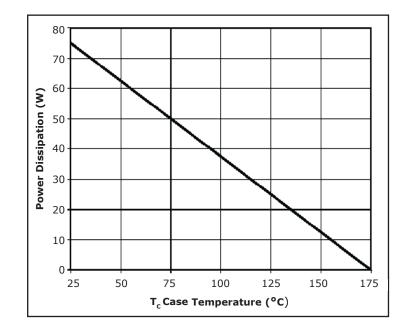
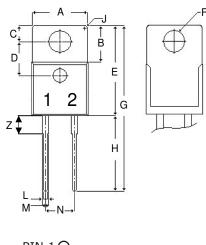


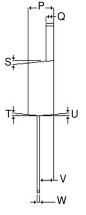
Figure 6. Power Derating



## **Package Dimensions**

## Package TO-220-2





m

m



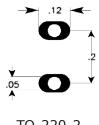
	POS	Inc	hes	Millimeters		
	P05	Min	Max	Min	Max	
	А	.381	.410	9.677	10.414	
	В	.235	.255	5.969	6.477	
	С	.100	.120	2.540	3.048	
	D	.223	.337	5.664	8.560	
	Е	.590	.615	14.986	15.621	
←X	F	.143	.153	3.632	3.886	
2	G	1.105	1.147	28.067	29.134	
į.	Н	.500	.550	12.700	13.970	
	J	R 0.	197	R 0.197		
	L	.025	.036	.635	.914	
	М	.045	.055	1.143	1.397	
	N	.195	.205	4.953	5.207	
	Р	.165	.185	4.191	4.699	
	Q	.048	.054	1.219	1.372	
	S	3°	6°	3°	6°	
	Т	3°	6°	3°	6°	
	U	3°	6°	3°	6°	
	V	.094	.110	2.388	2.794	
	W	.014	.025	.356	.635	
	Х	3°	5.5°	3°	5.5°	
	Y	.385	.410	9.779	10.414	
	z	.130	.150	3.302	3.810	

NOTE: 1. Dimension L, M, W apply for Solder Dip Finish

5



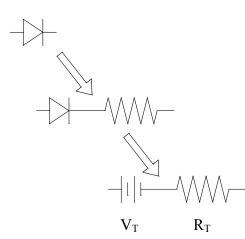
#### **Recommended Solder Pad Layout**



10-220-2
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Part Number	Package	Marking
C3D04065A	TO-220-2	C3D04065

#### **Diode Model**



 $Vf_T = V_T + If^*R_T$ 

$$V_{T} = 0.98 + (T_{J}^{*} - 1.8^{*}10^{-3})$$
  

$$R_{T} = 0.10 + (T_{J}^{*} 9.16^{*}10^{-4})$$

Note: T<sub>i</sub> = Diode Junction Temperature In Degrees Celcius

"The levels of environmentally sensitive, persistent biologically toxic (PBT), persistent organic pollutants (POP), or otherwise restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2002/95/EC on the restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS), as amended through April 21, 2006.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, air traffic control systems, or weapons systems.

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6